

FIG. 1

FIG. 1

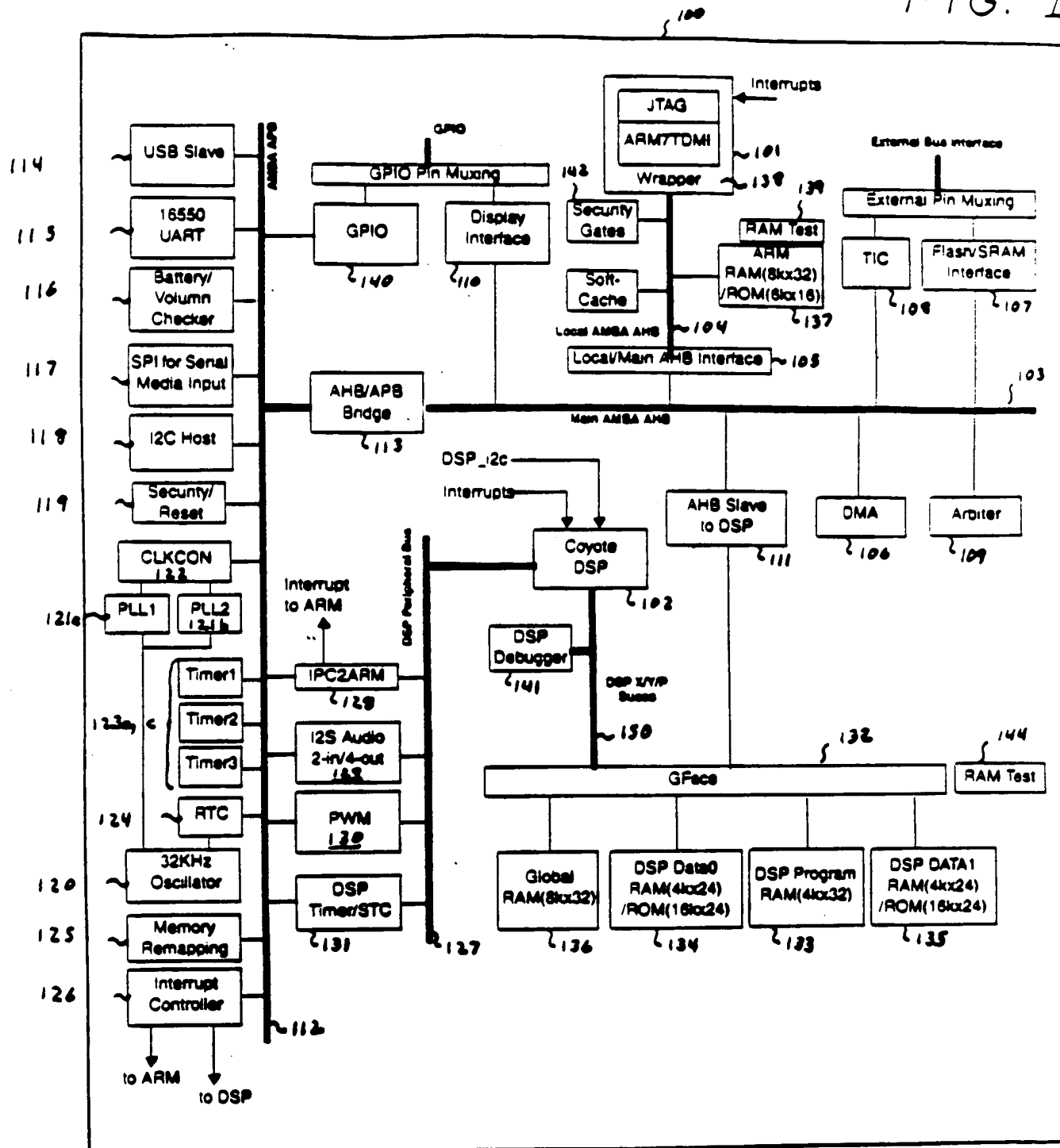


FIG. 2A

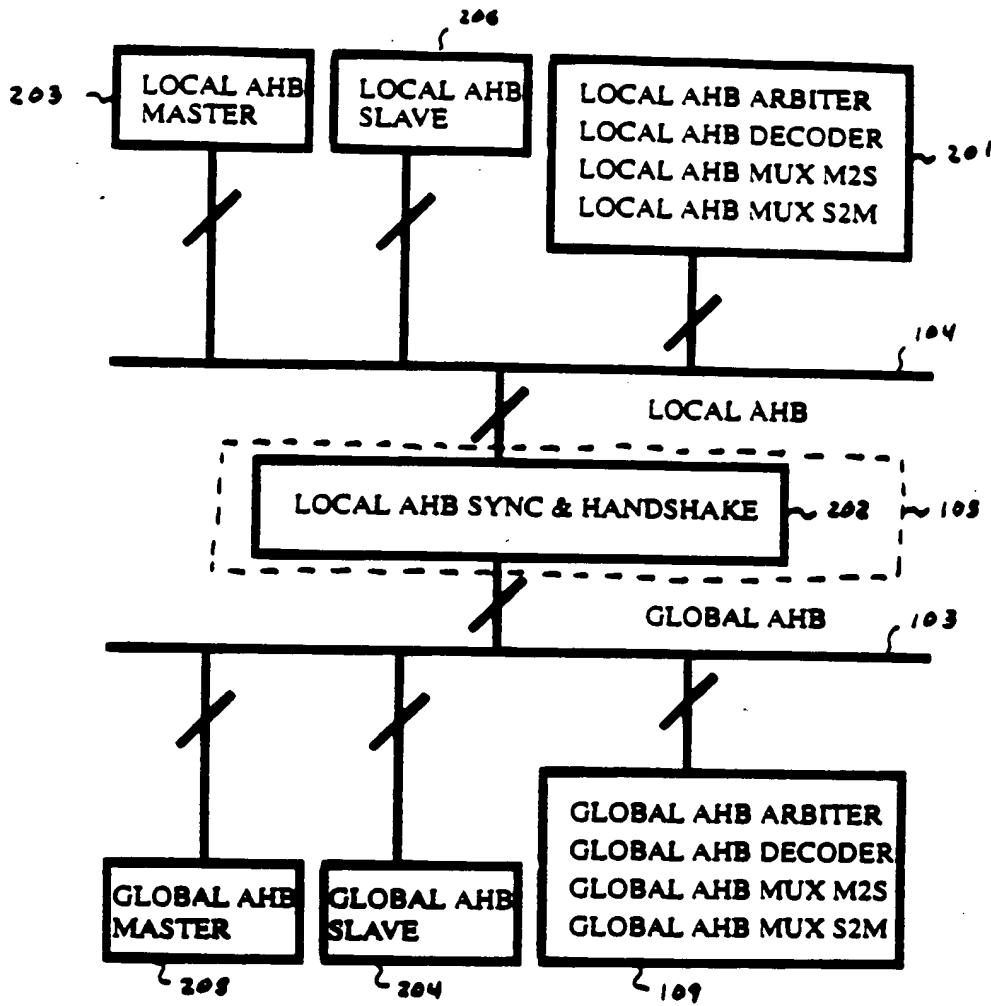


FIG. 2A

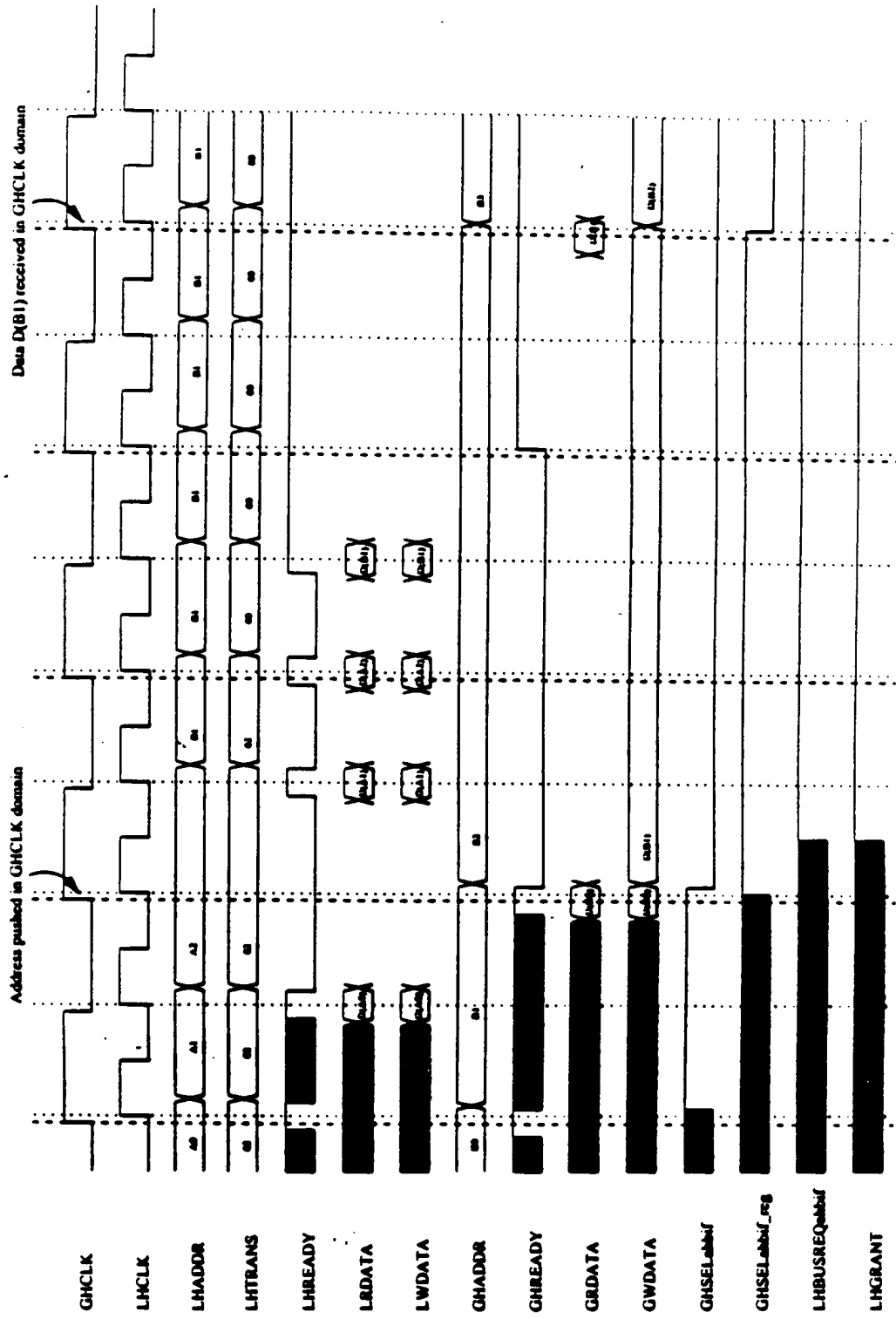


FIG. 2B

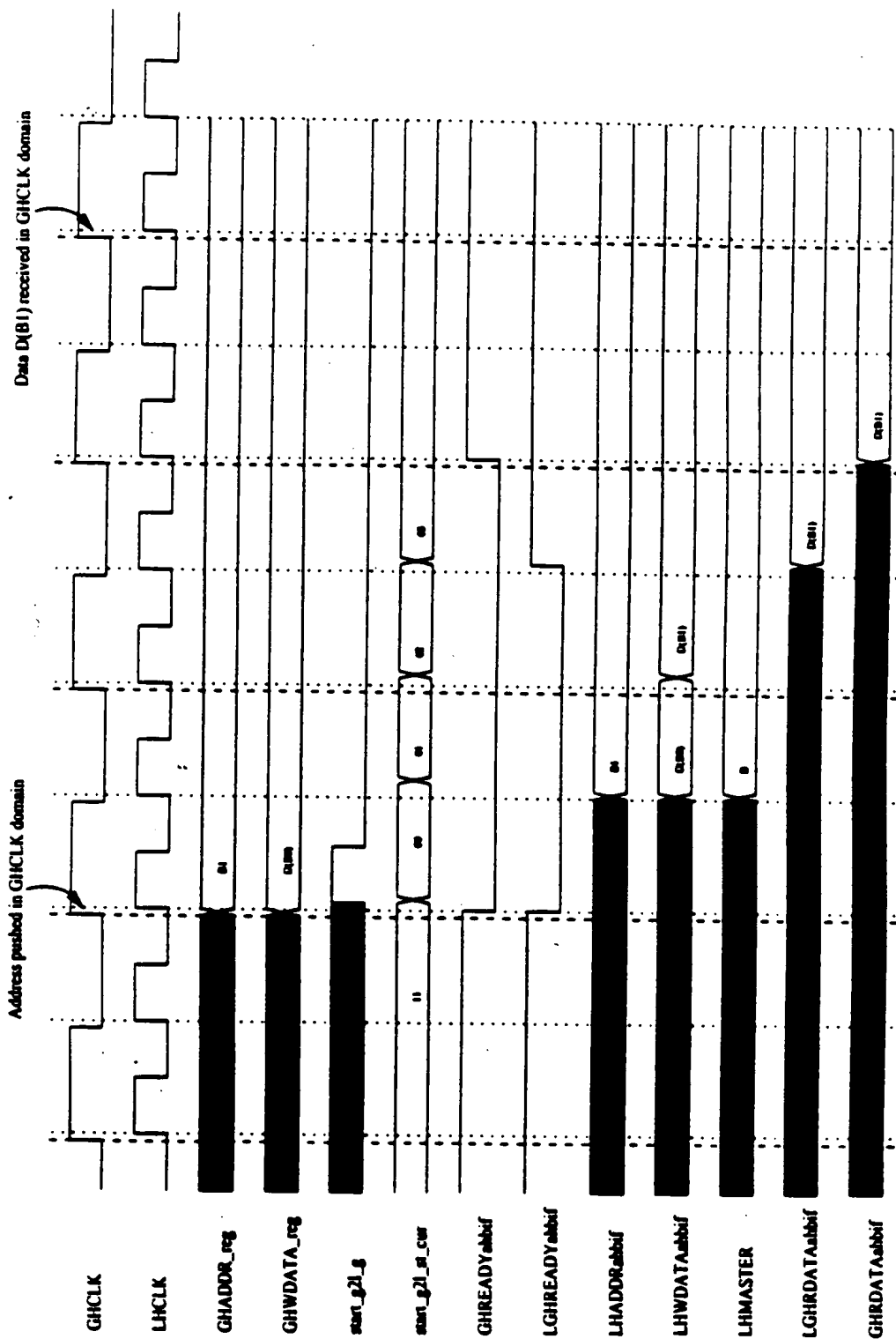
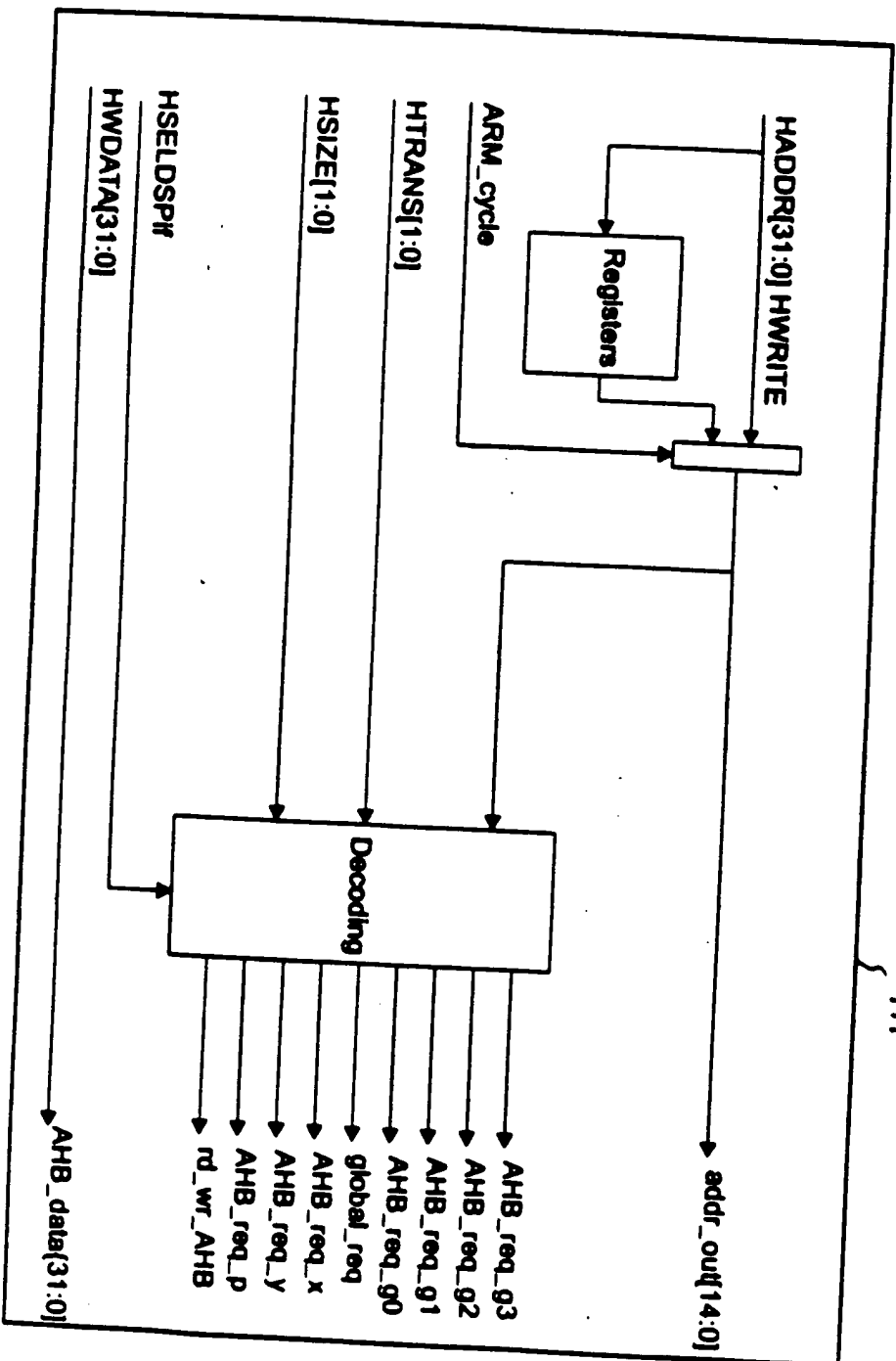


FIG. 2C



FIG. 3



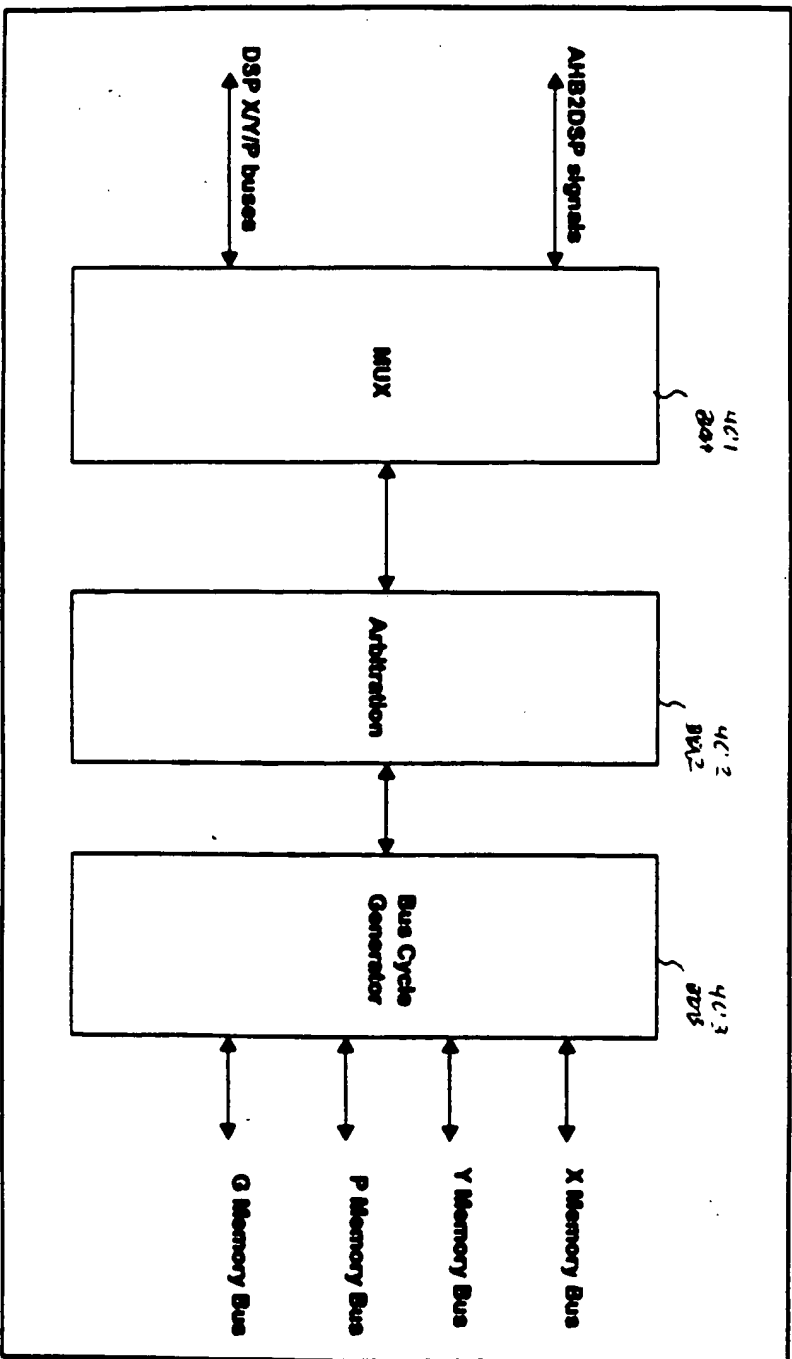


FIG. 4A

[illegible]

FIG. 4B

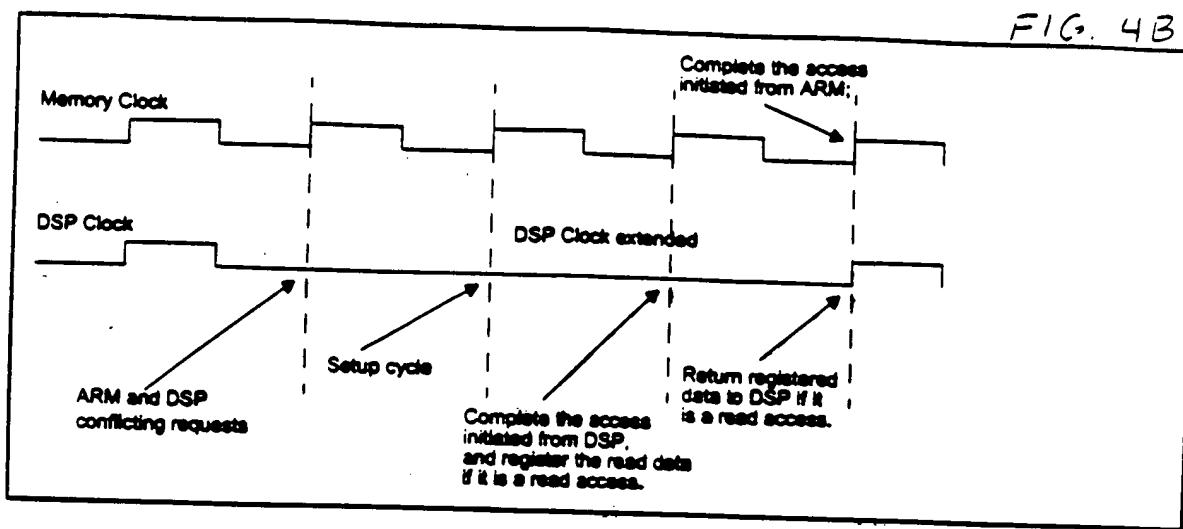
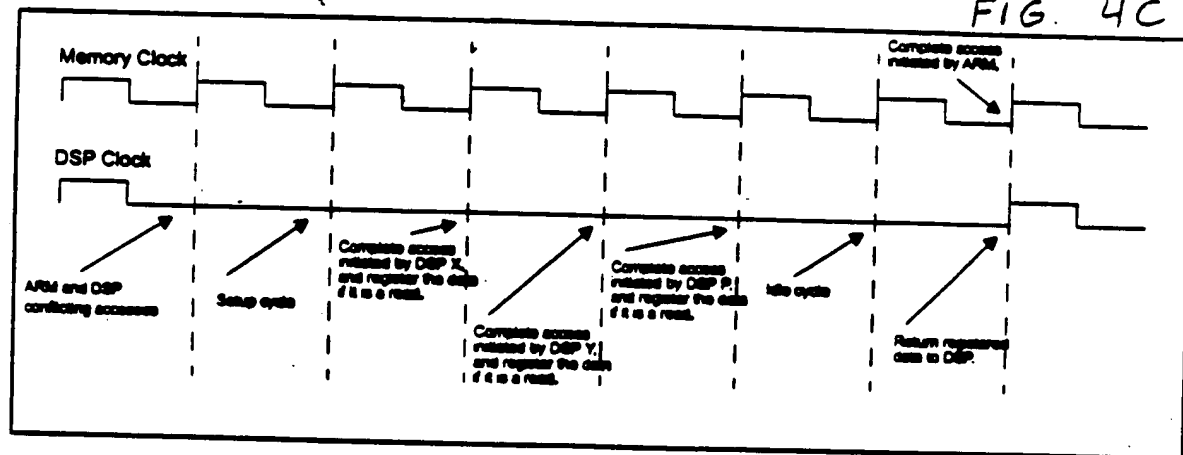
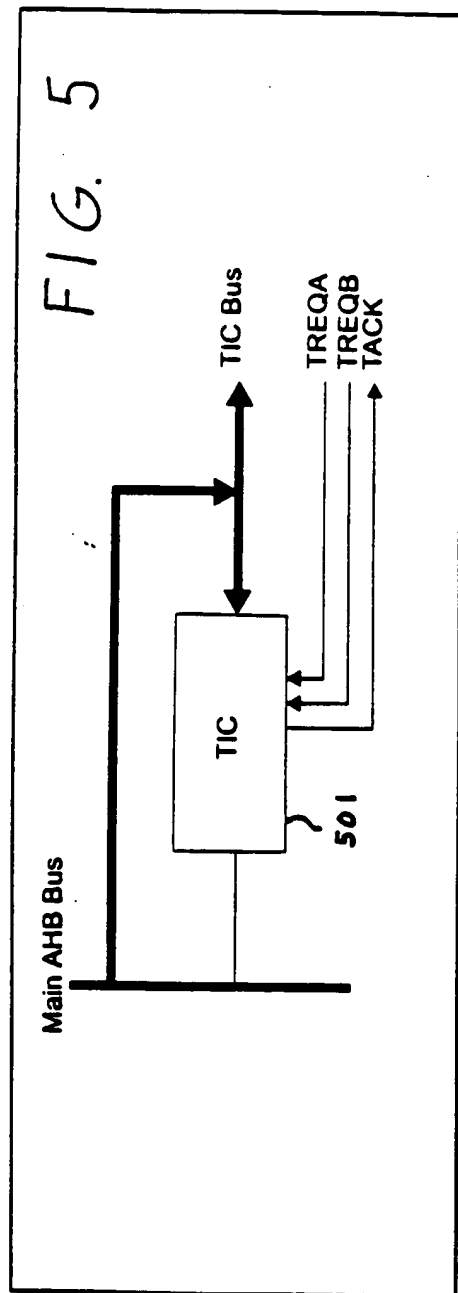
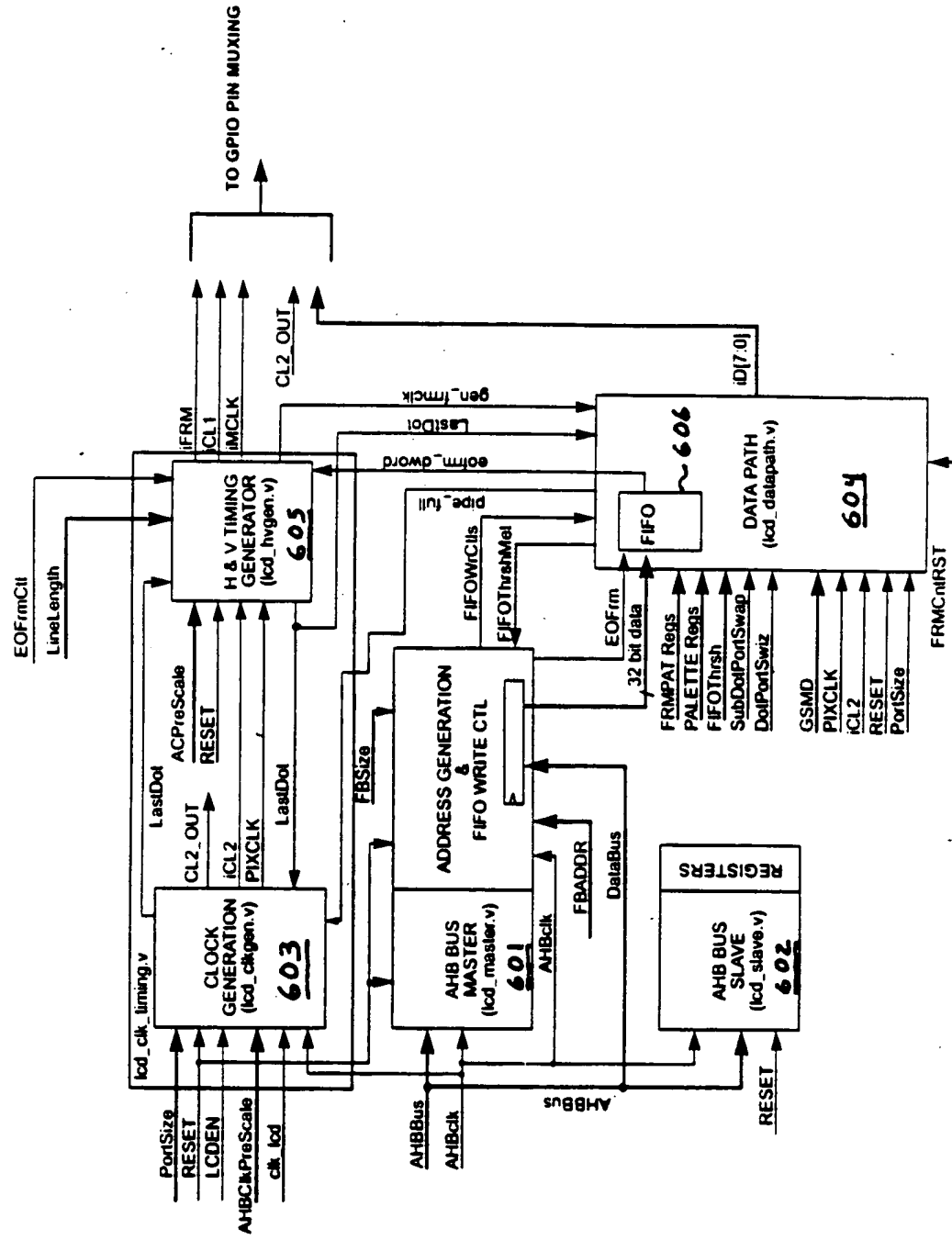


FIG. 4C









LCD Controller (lcd\_top.v) Block Diagram

FIG. 6A



FIG 6 E

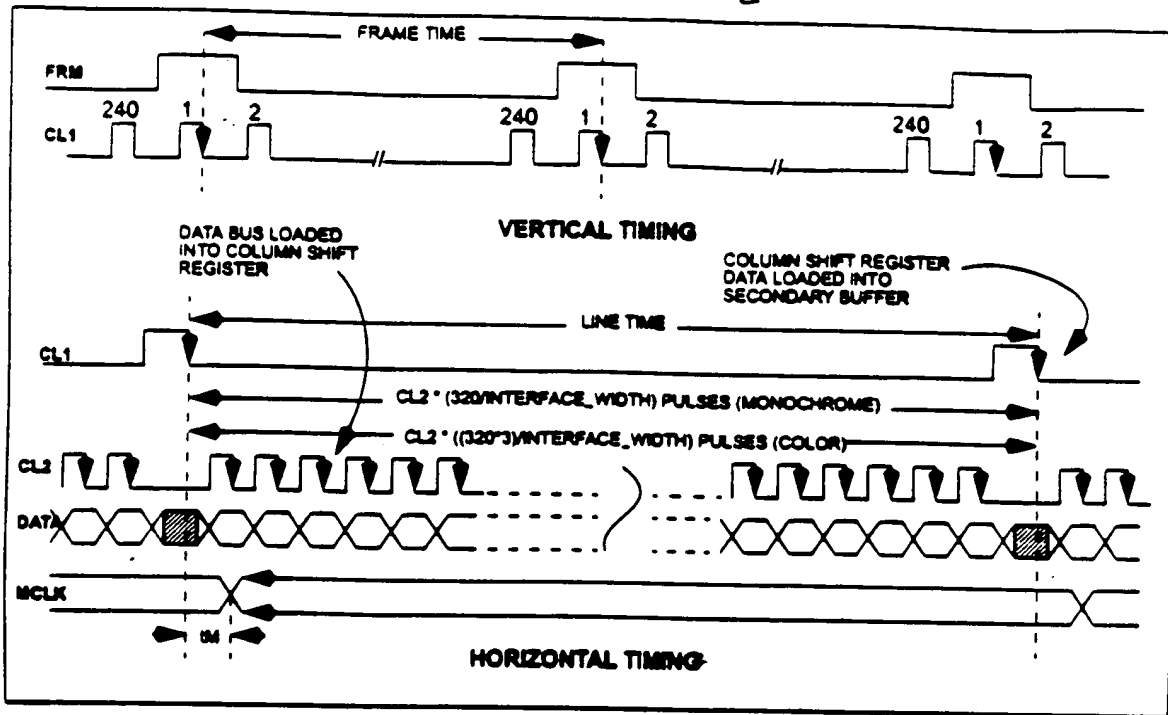


FIG. 6 D

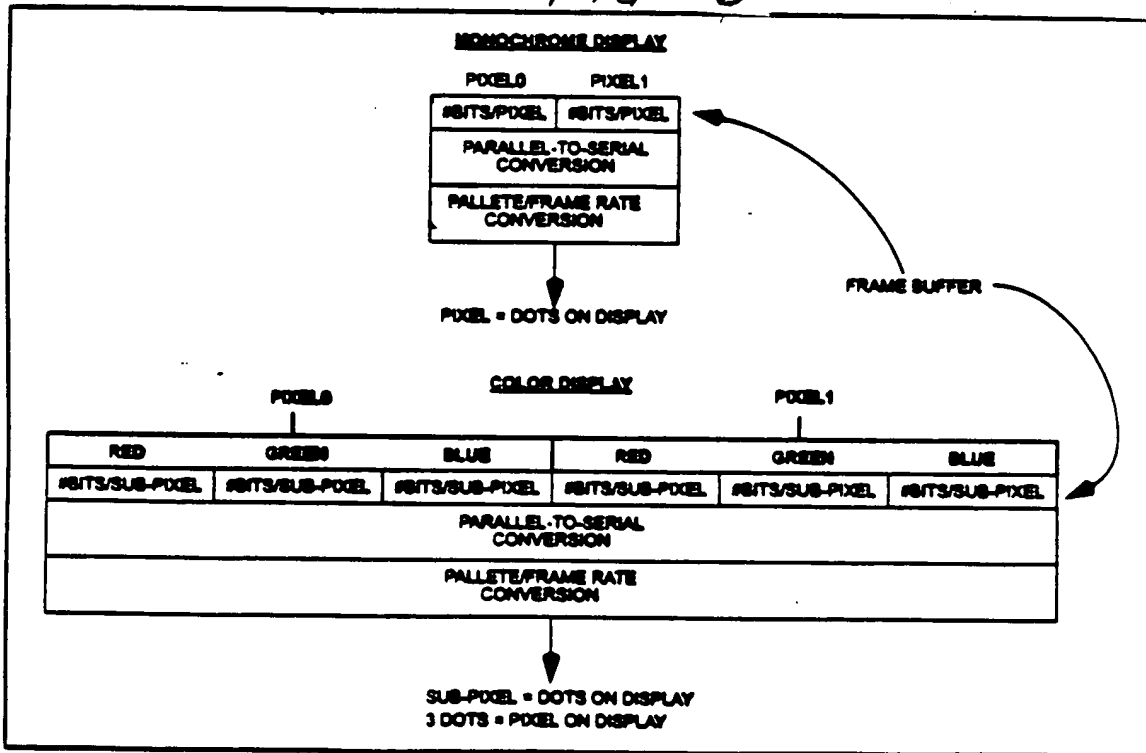


FIG. 6F

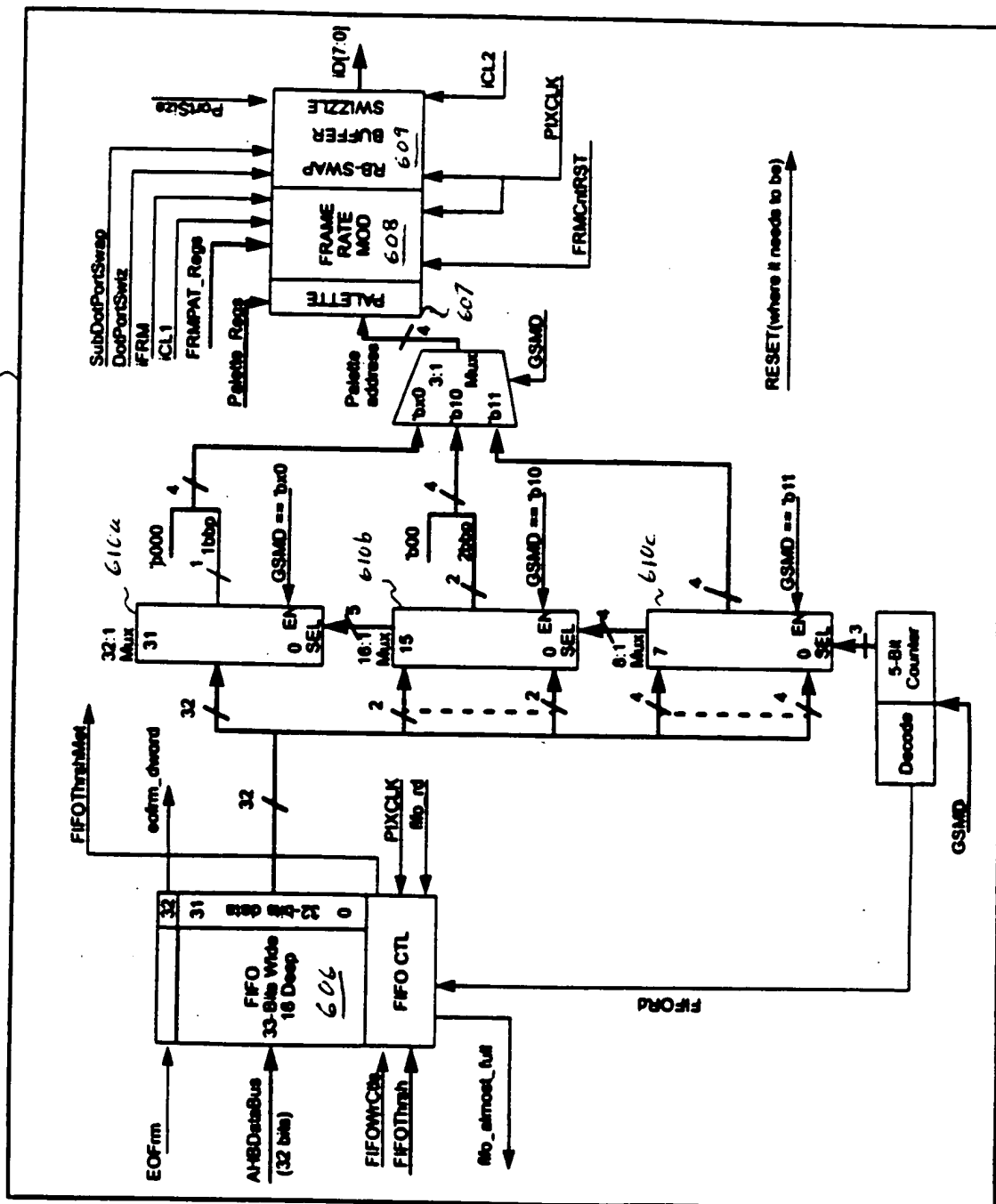


FIG 6 G

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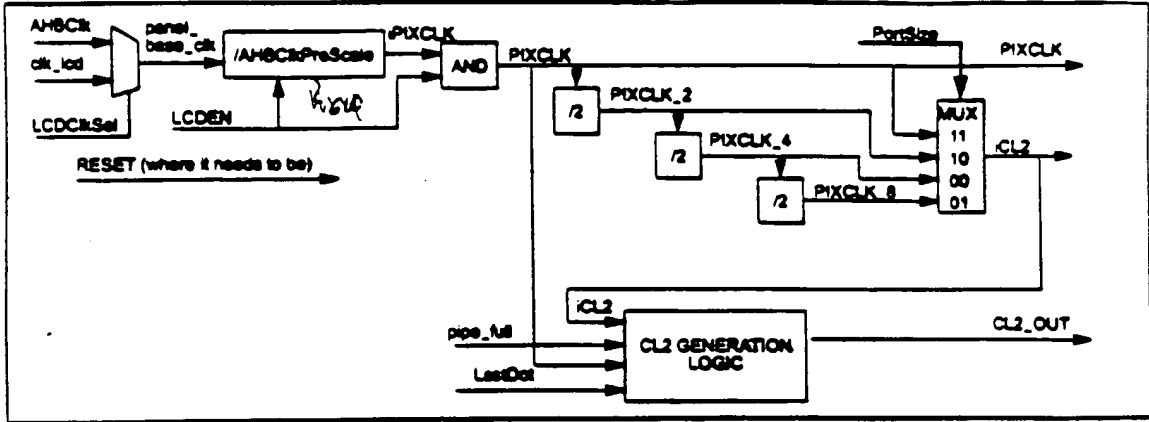
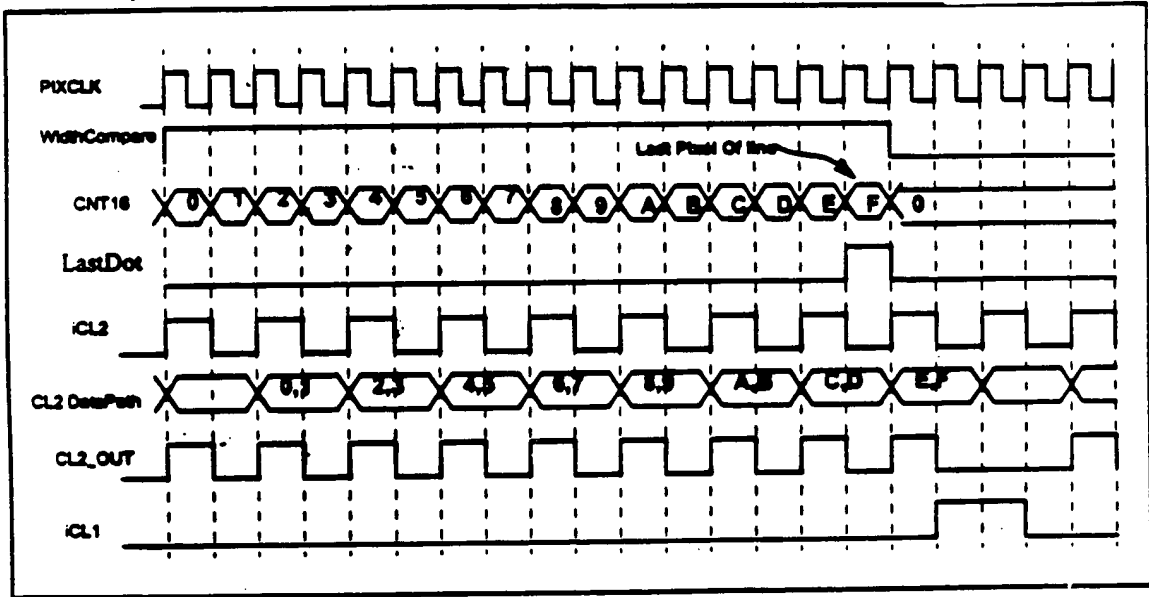


FIG 6 H



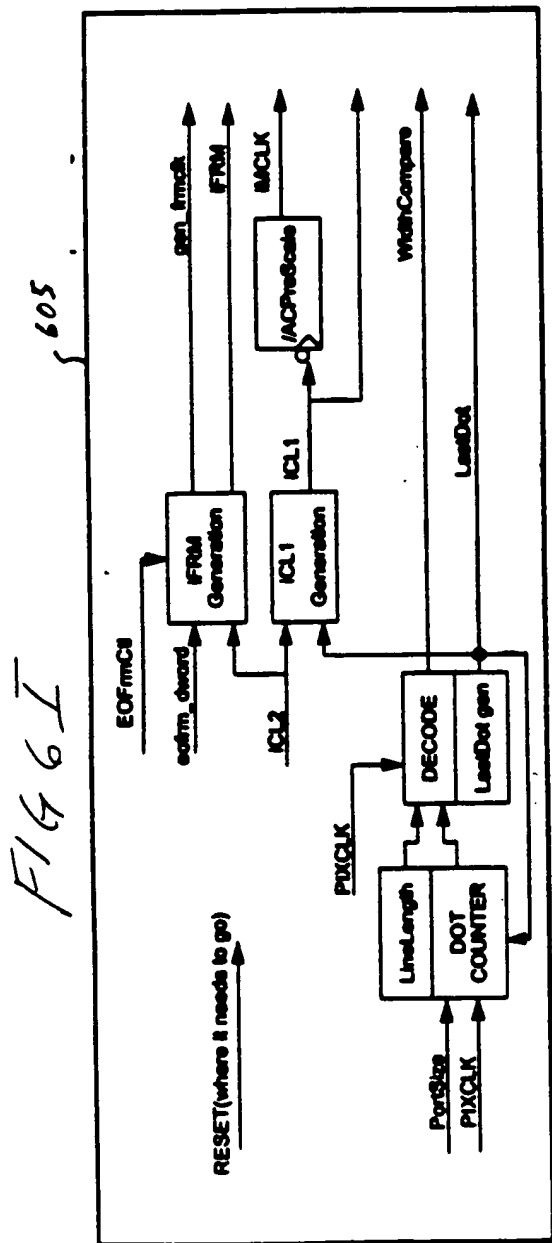
[illegible]

FIG 67

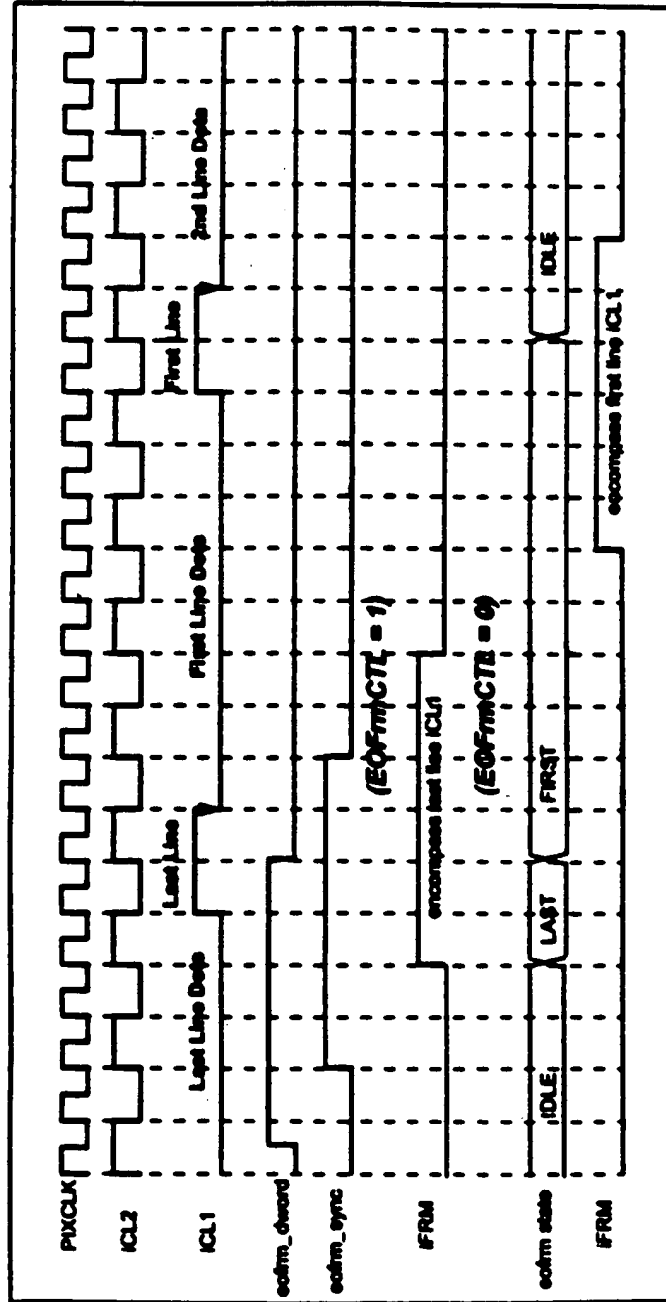




FIG. 6K

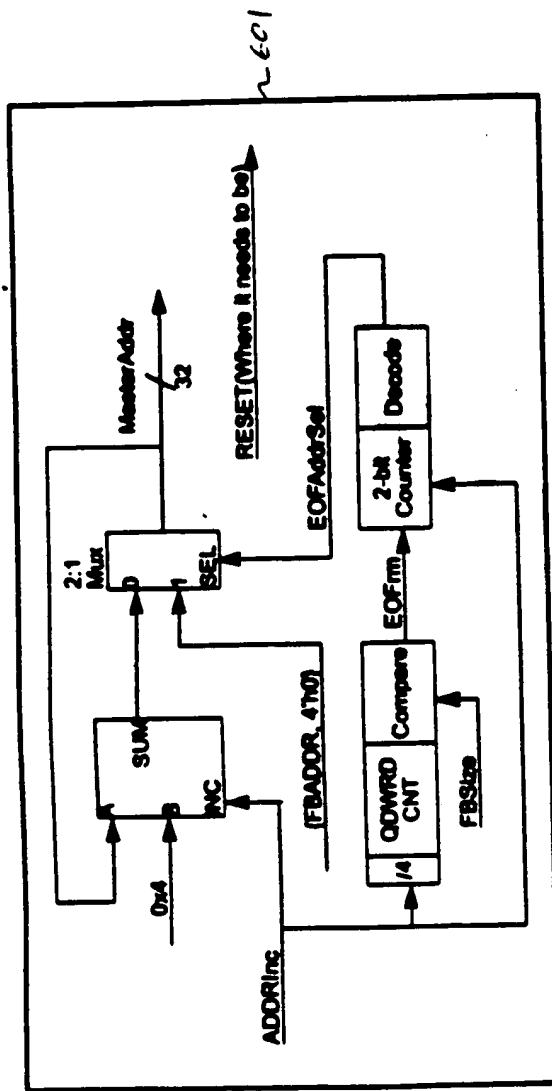


FIG 6L

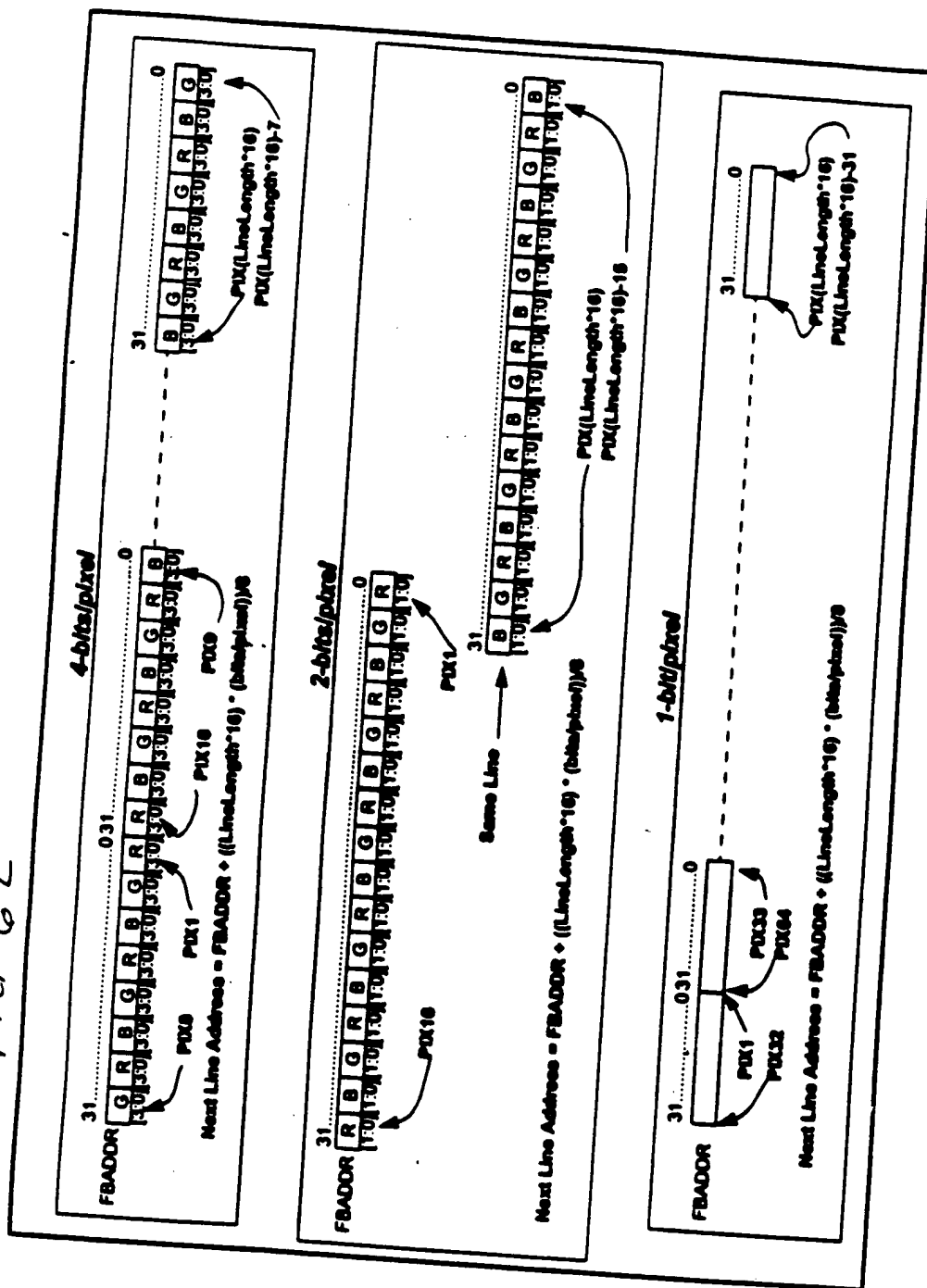


FIG 6M

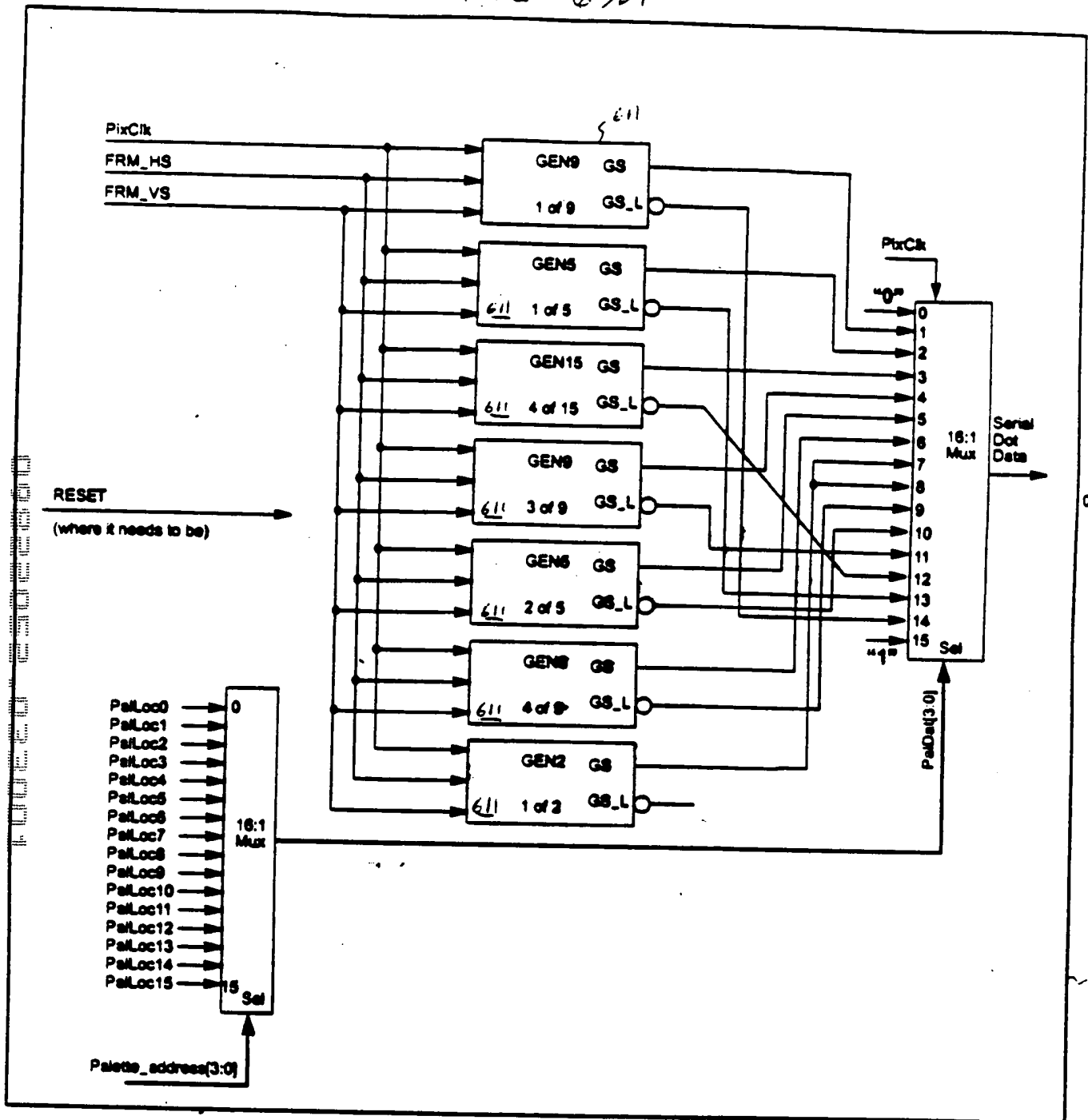
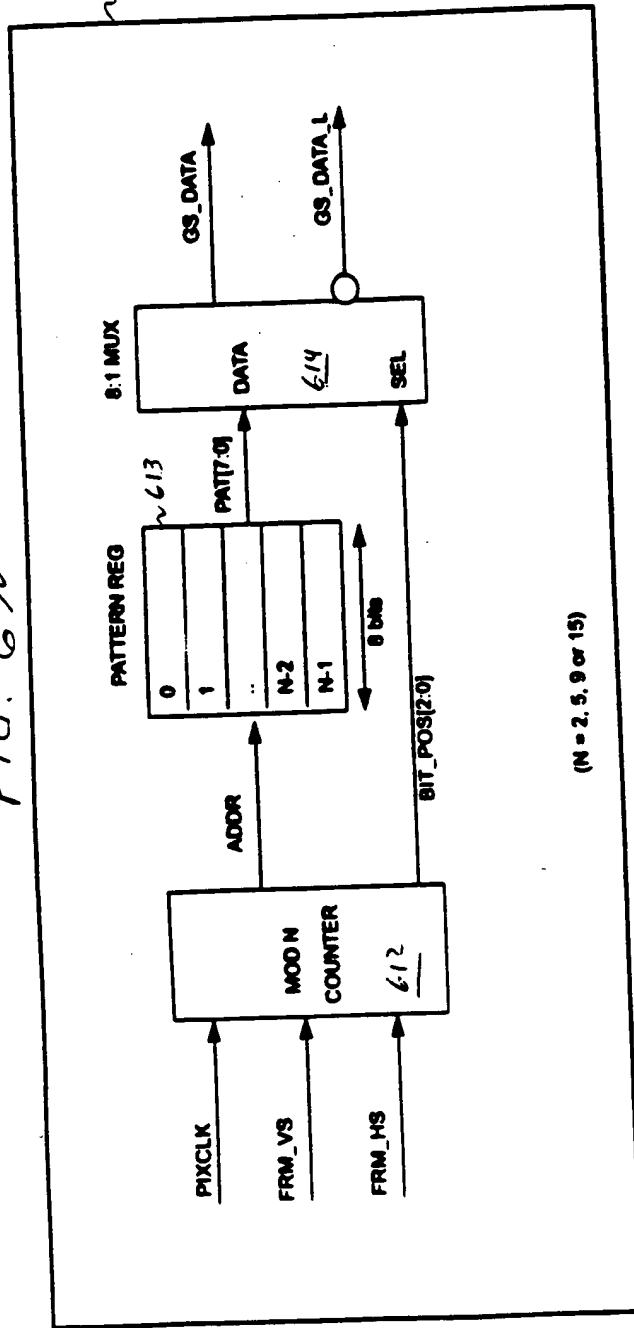


FIG. 6N



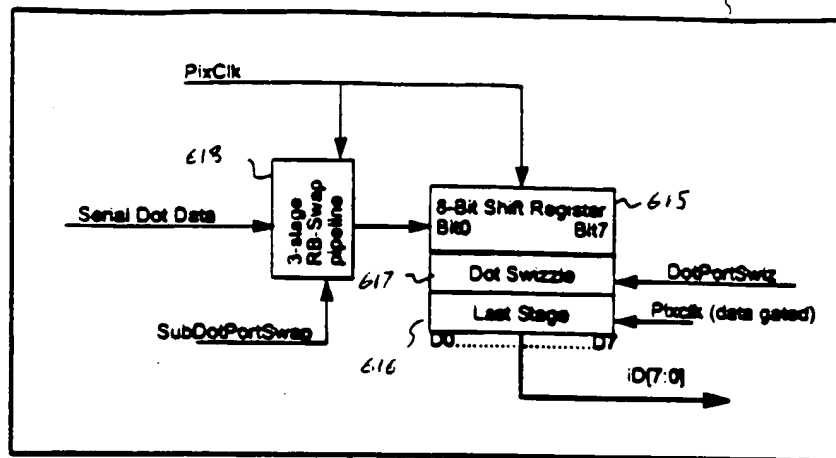
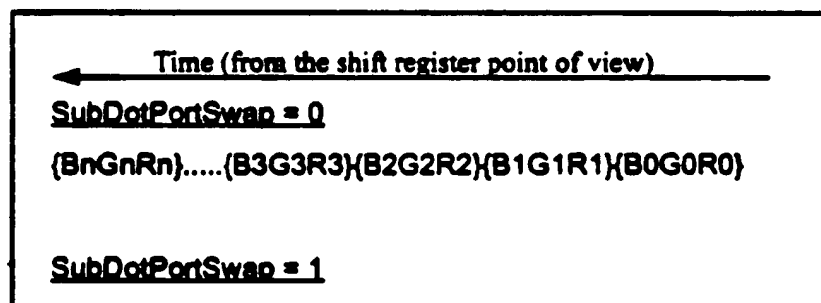


FIG 60

FIG. 6P



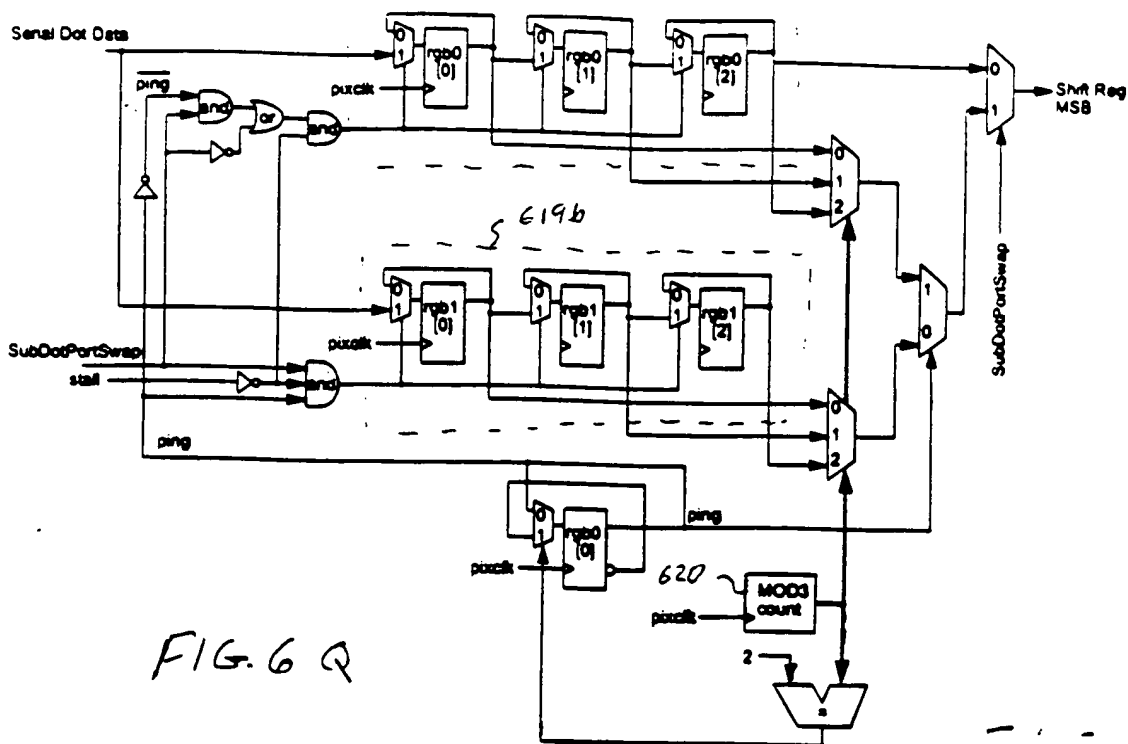


FIG 6.R

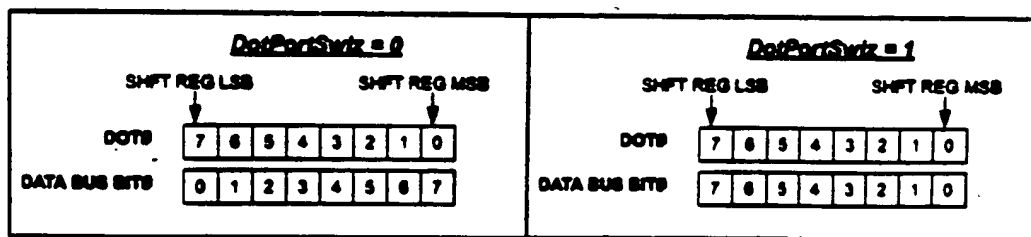


FIG. 6 S

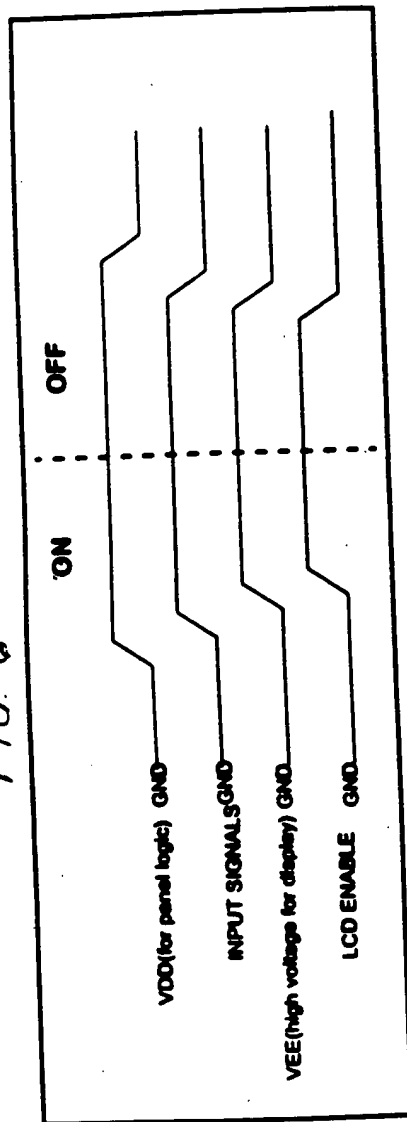


FIG. 7A

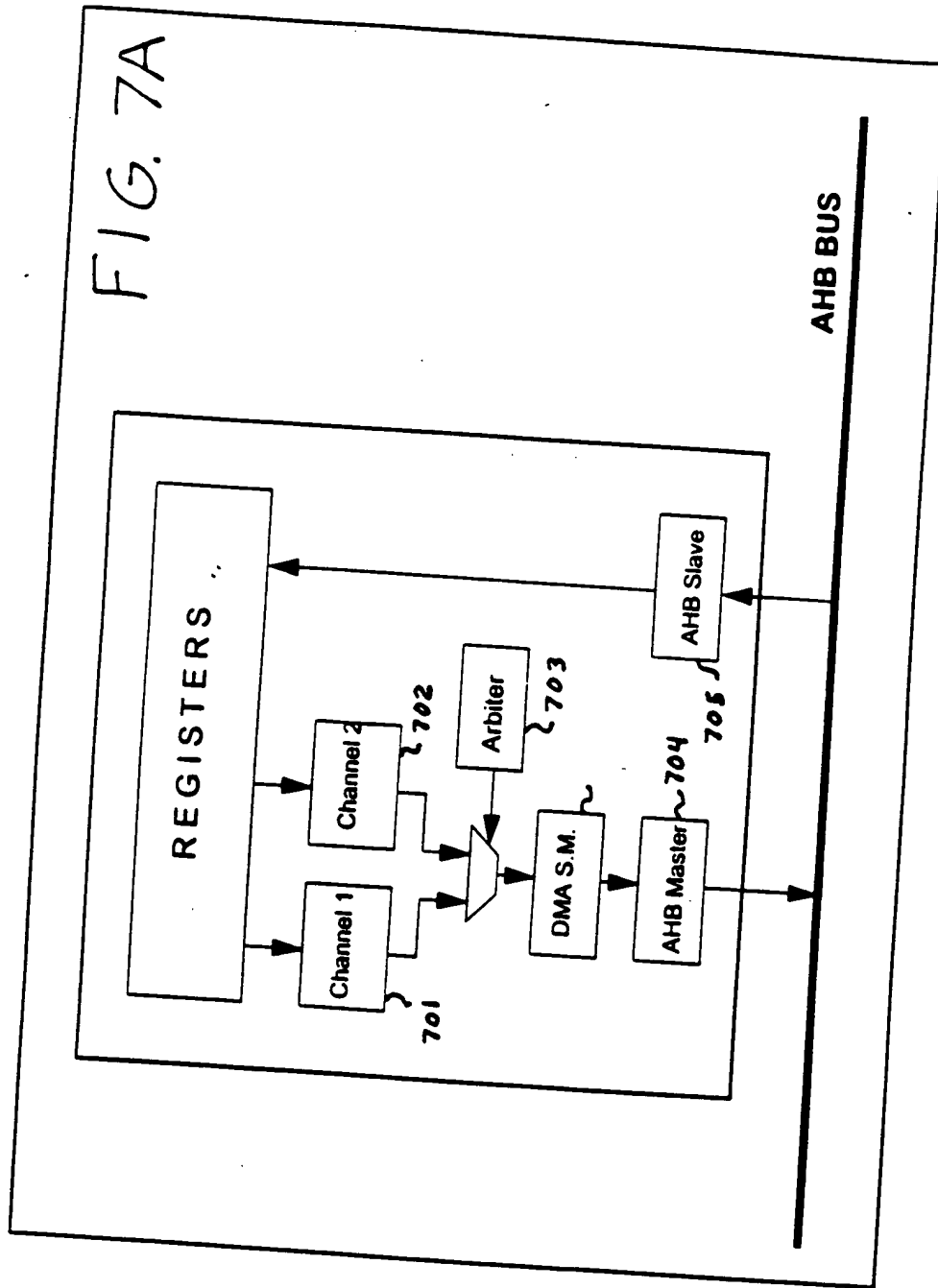




FIG. 7B

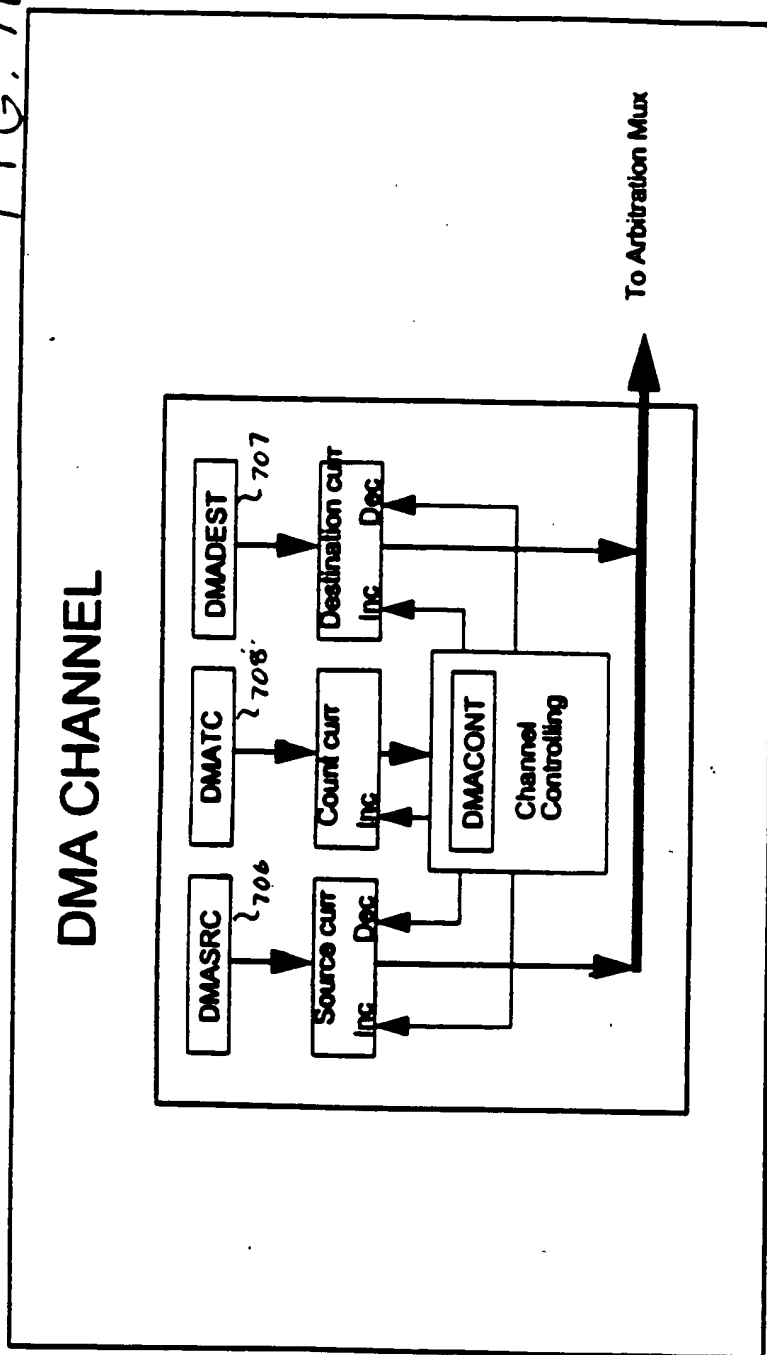
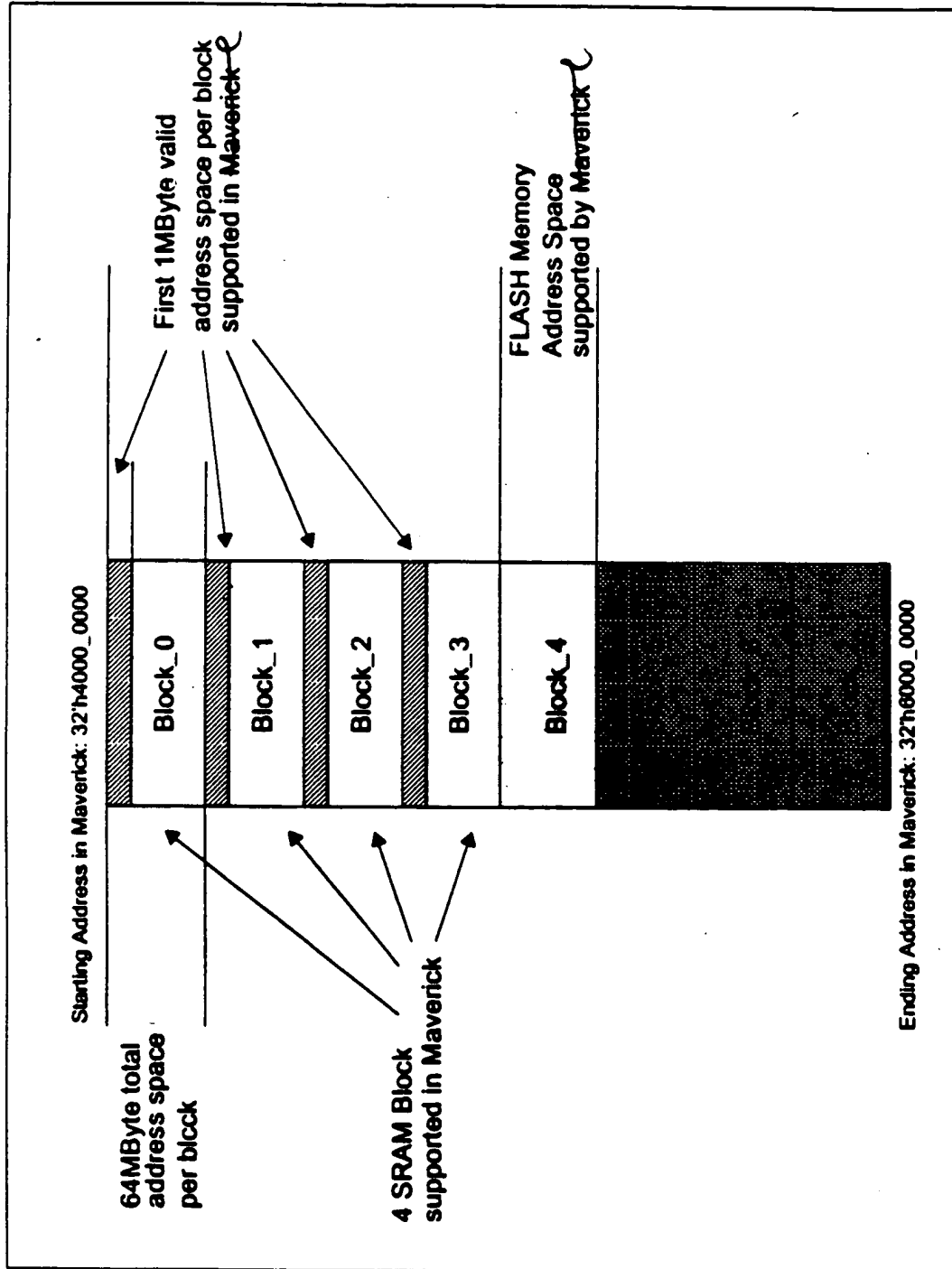


FIG. 8



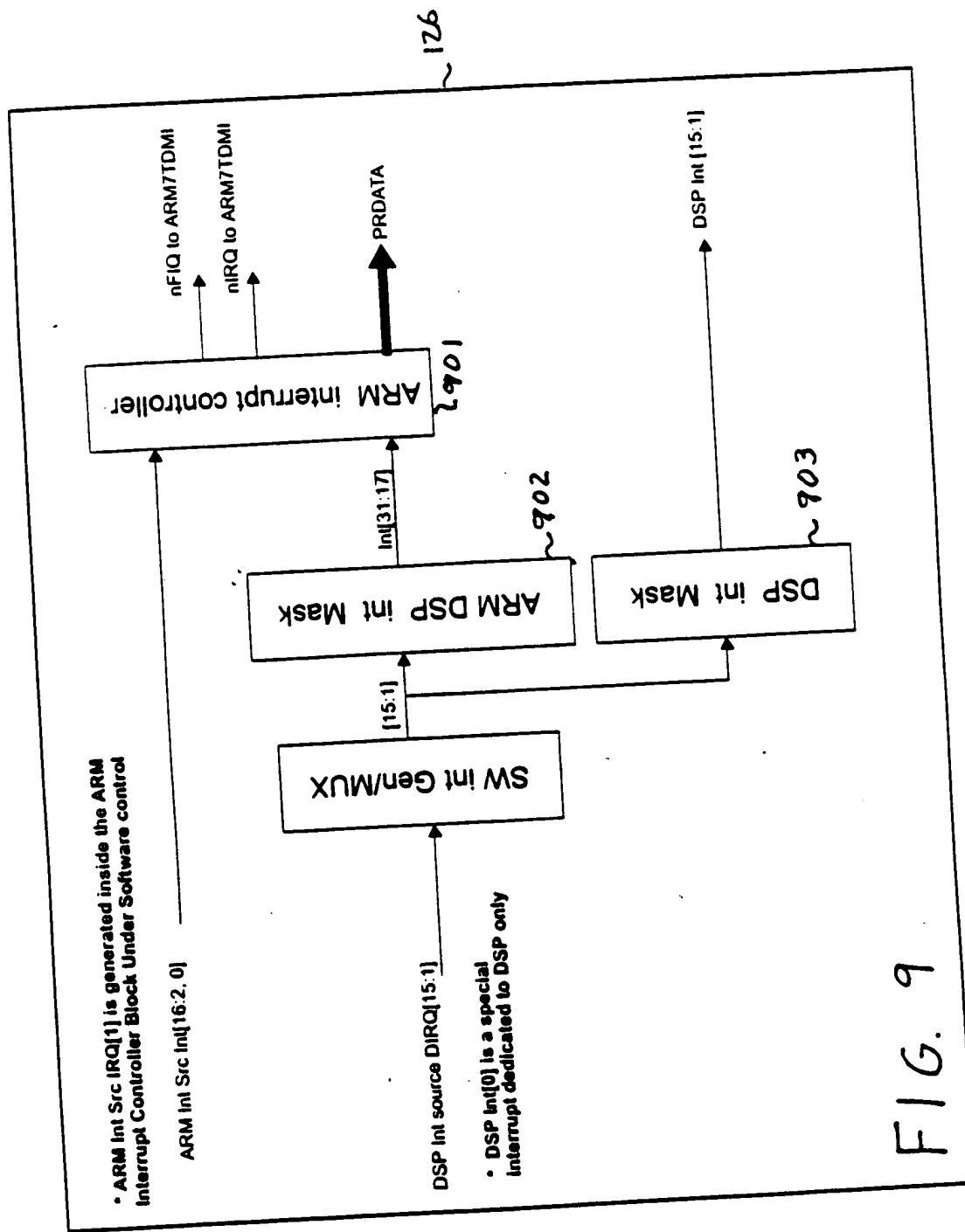


FIG. 9

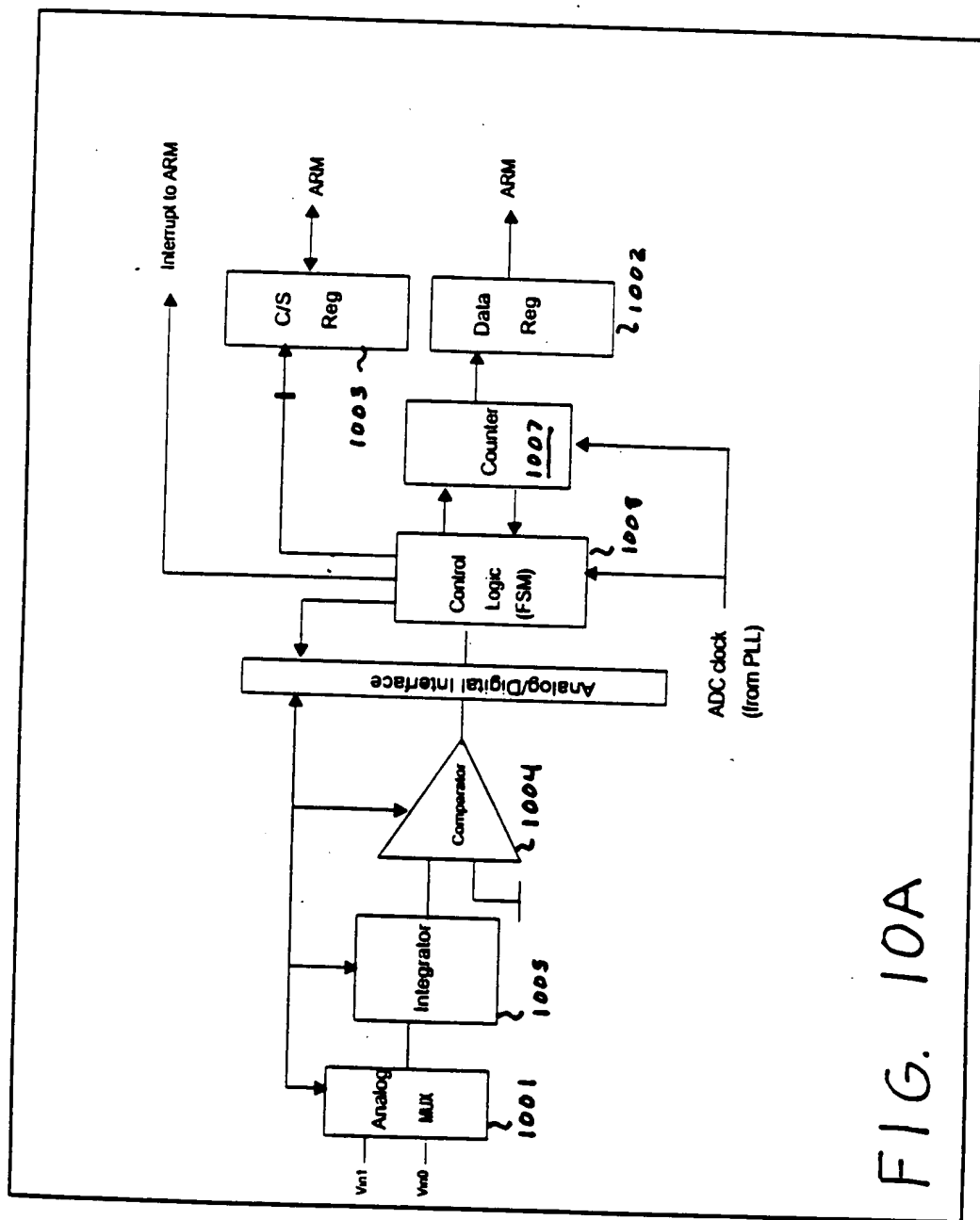


FIG. 10A

FIG. 10B

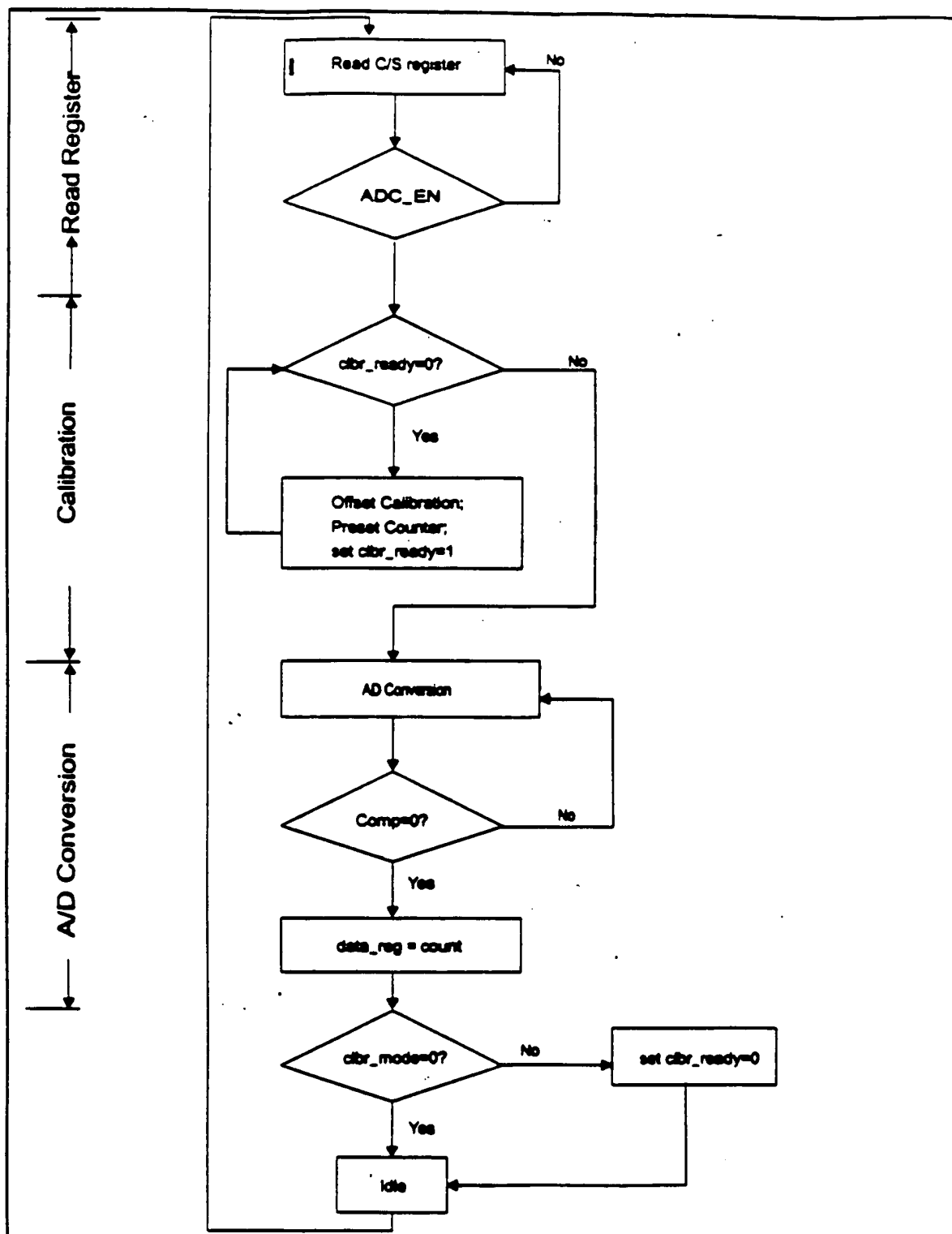
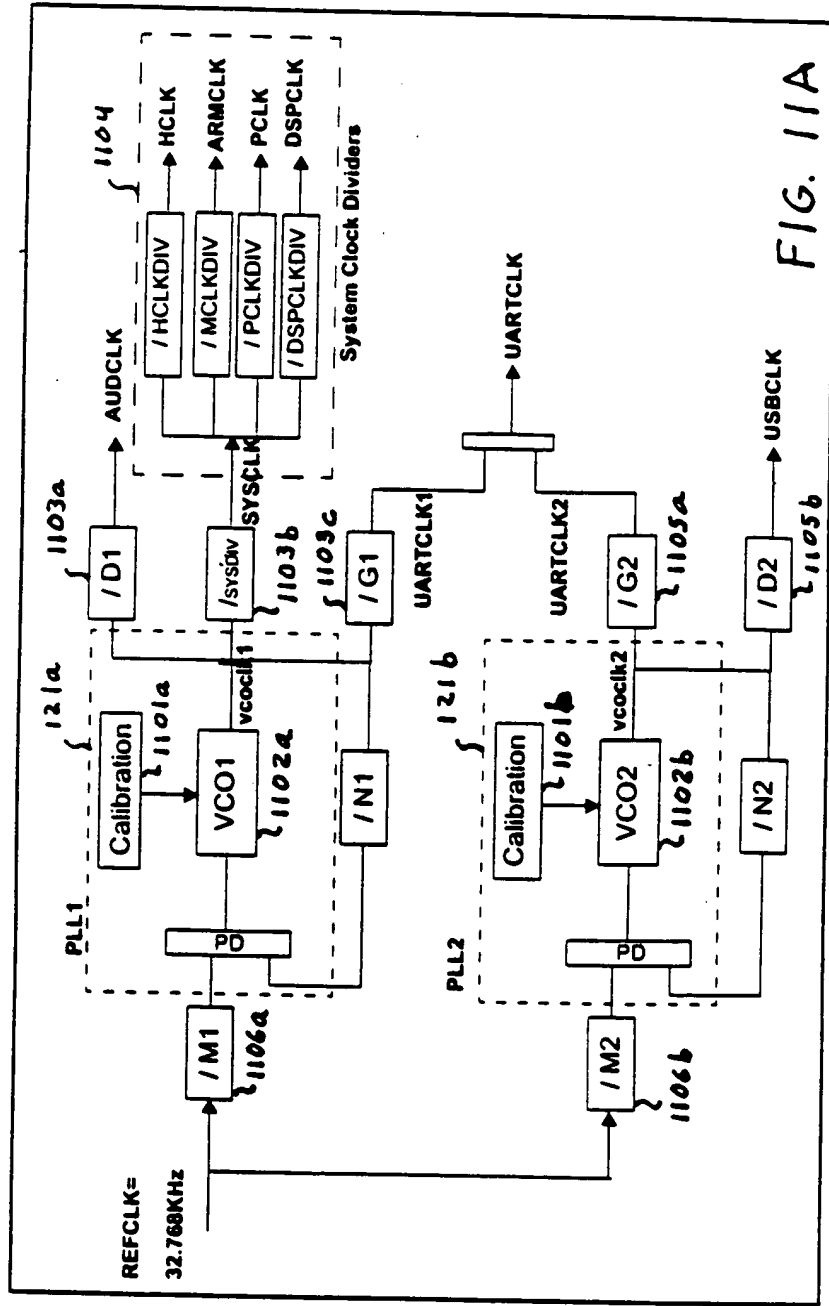
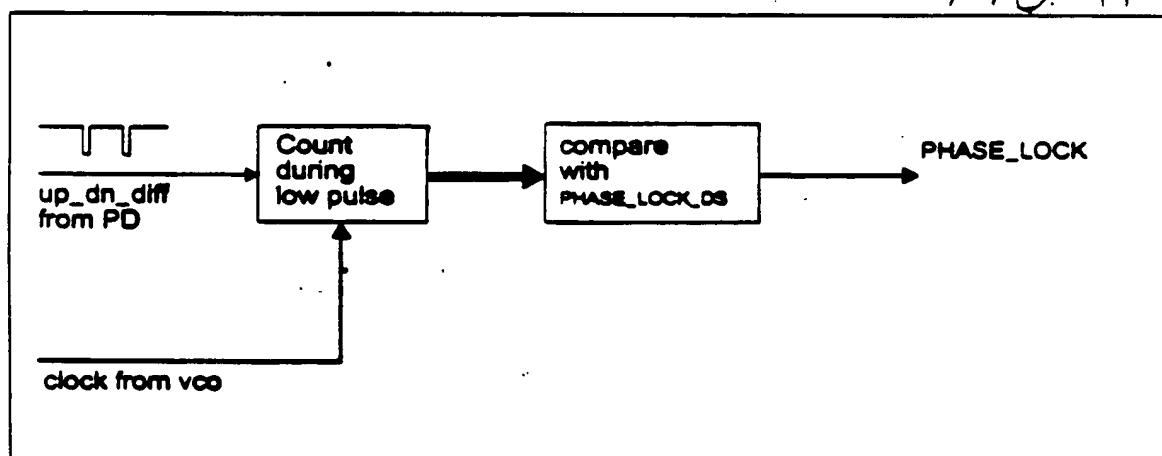
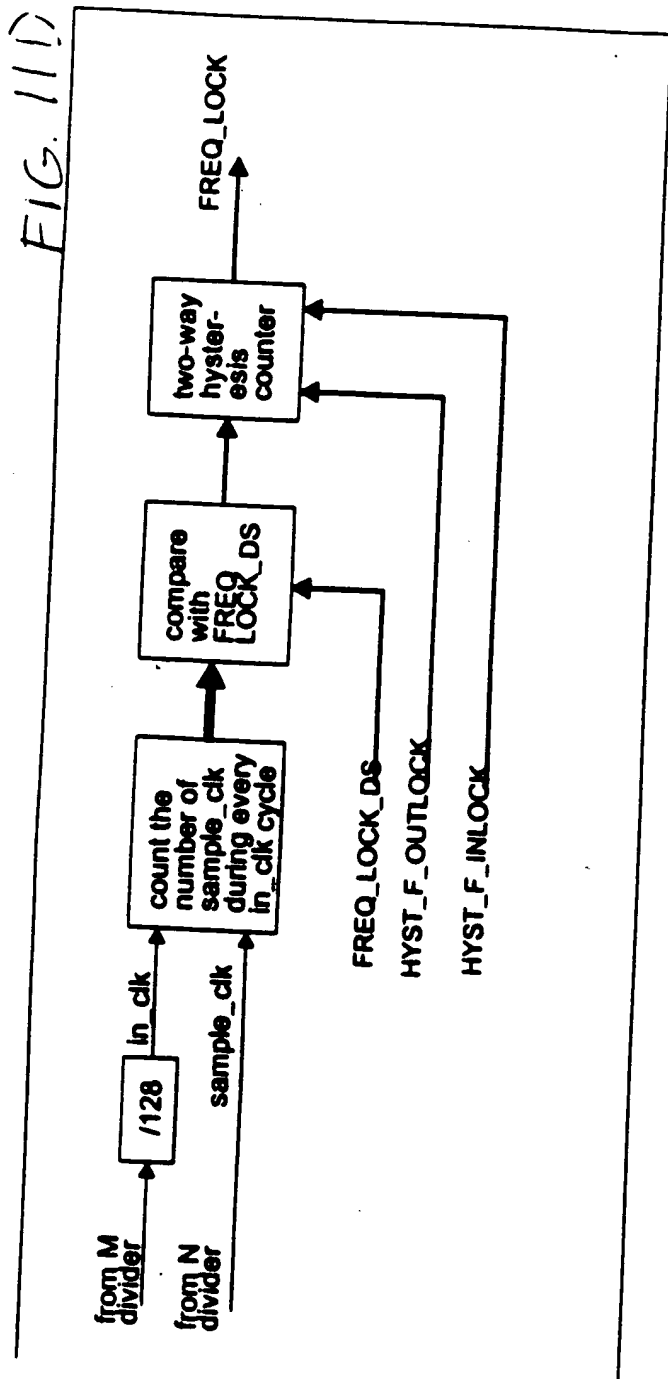


FIG. 10B









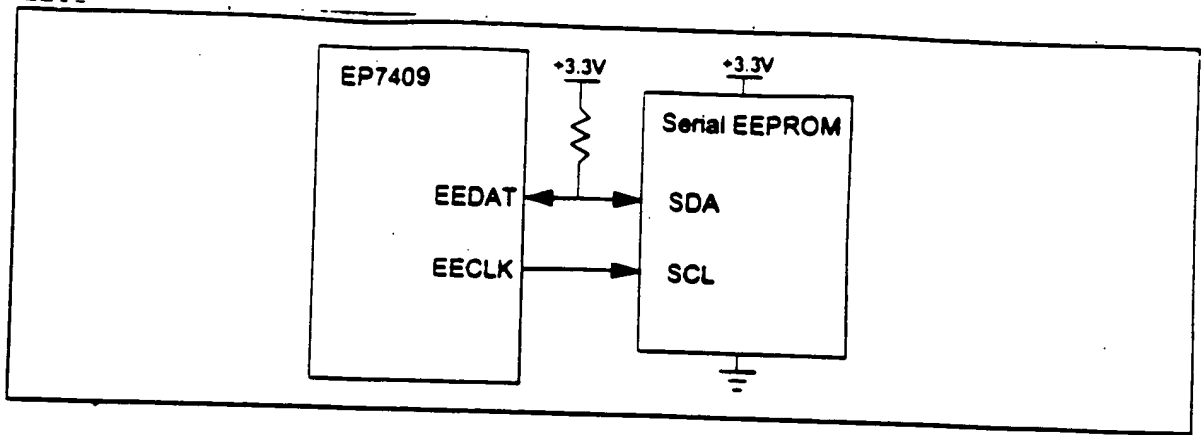


Fig. 12 A

Fig 12 B

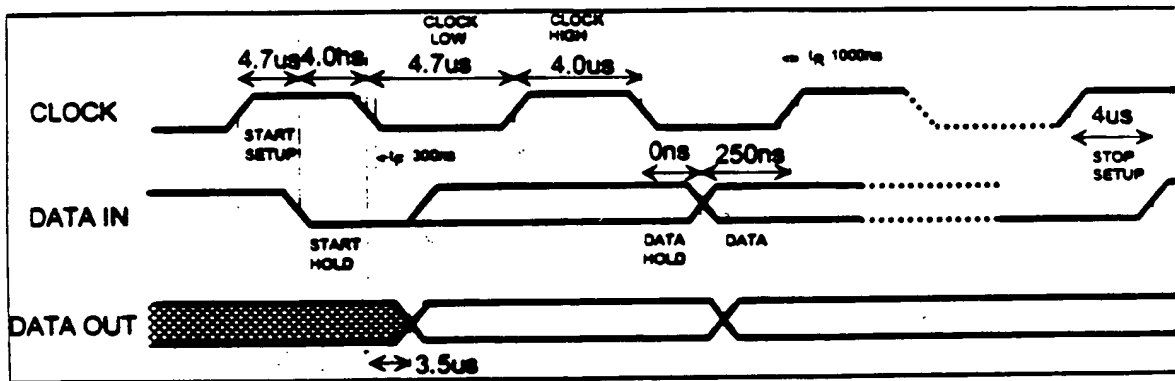
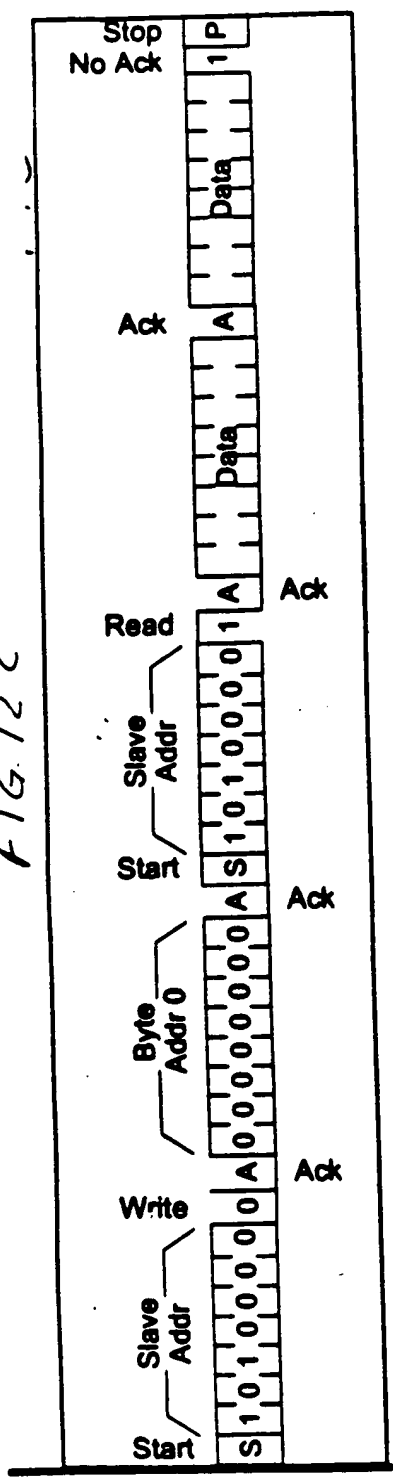


FIG. 12C

FIG. 12C



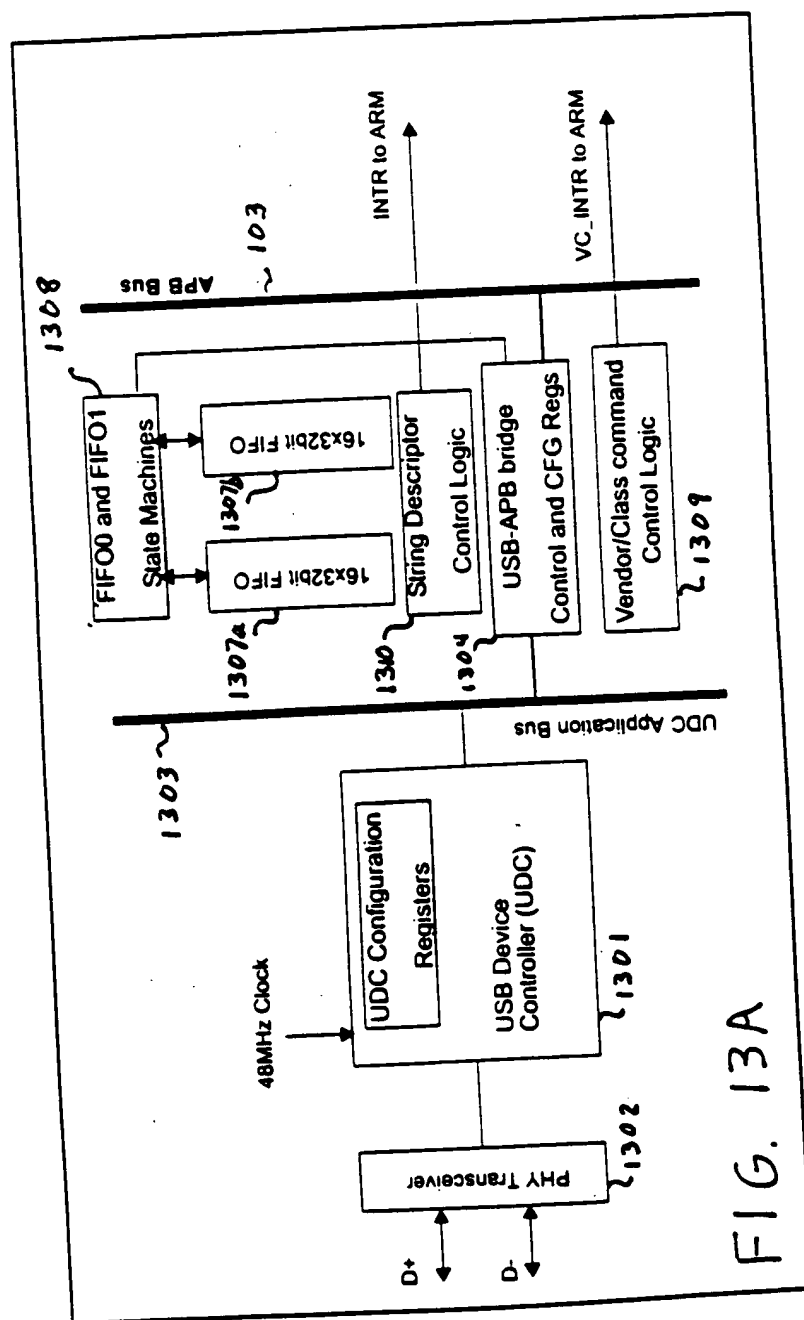
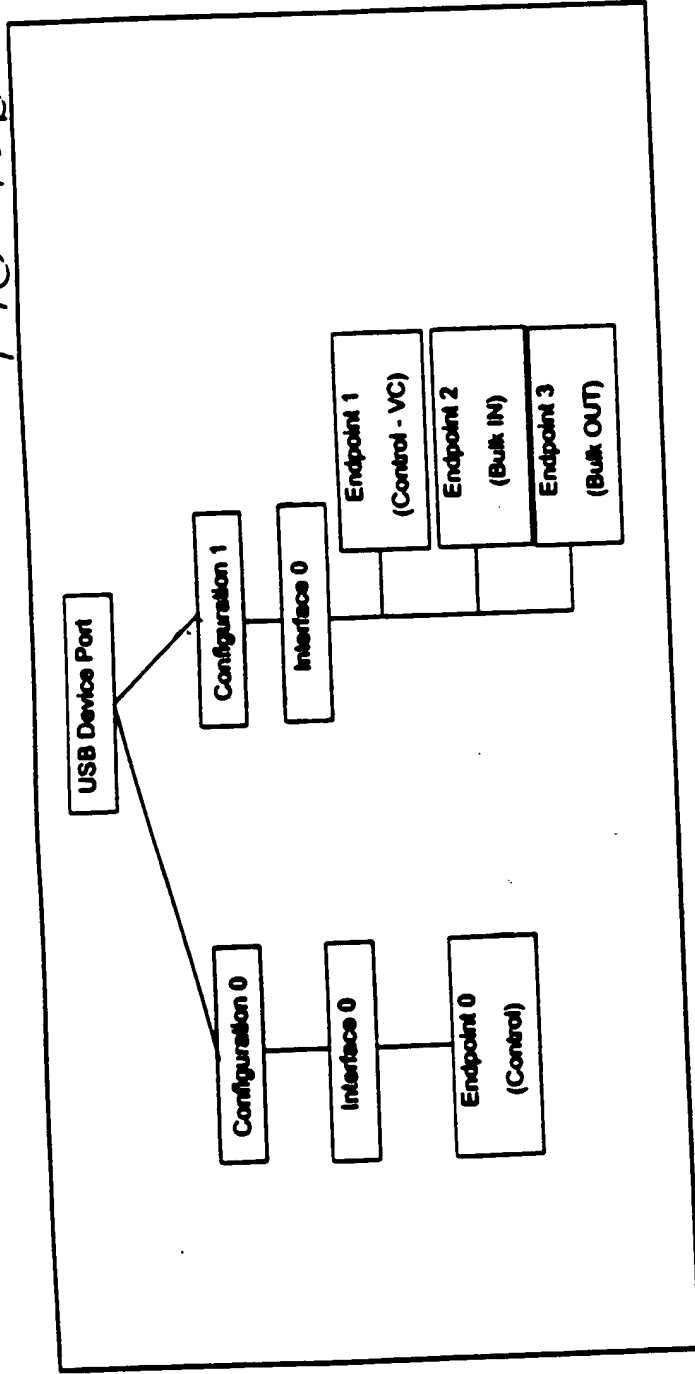


FIG. 13B



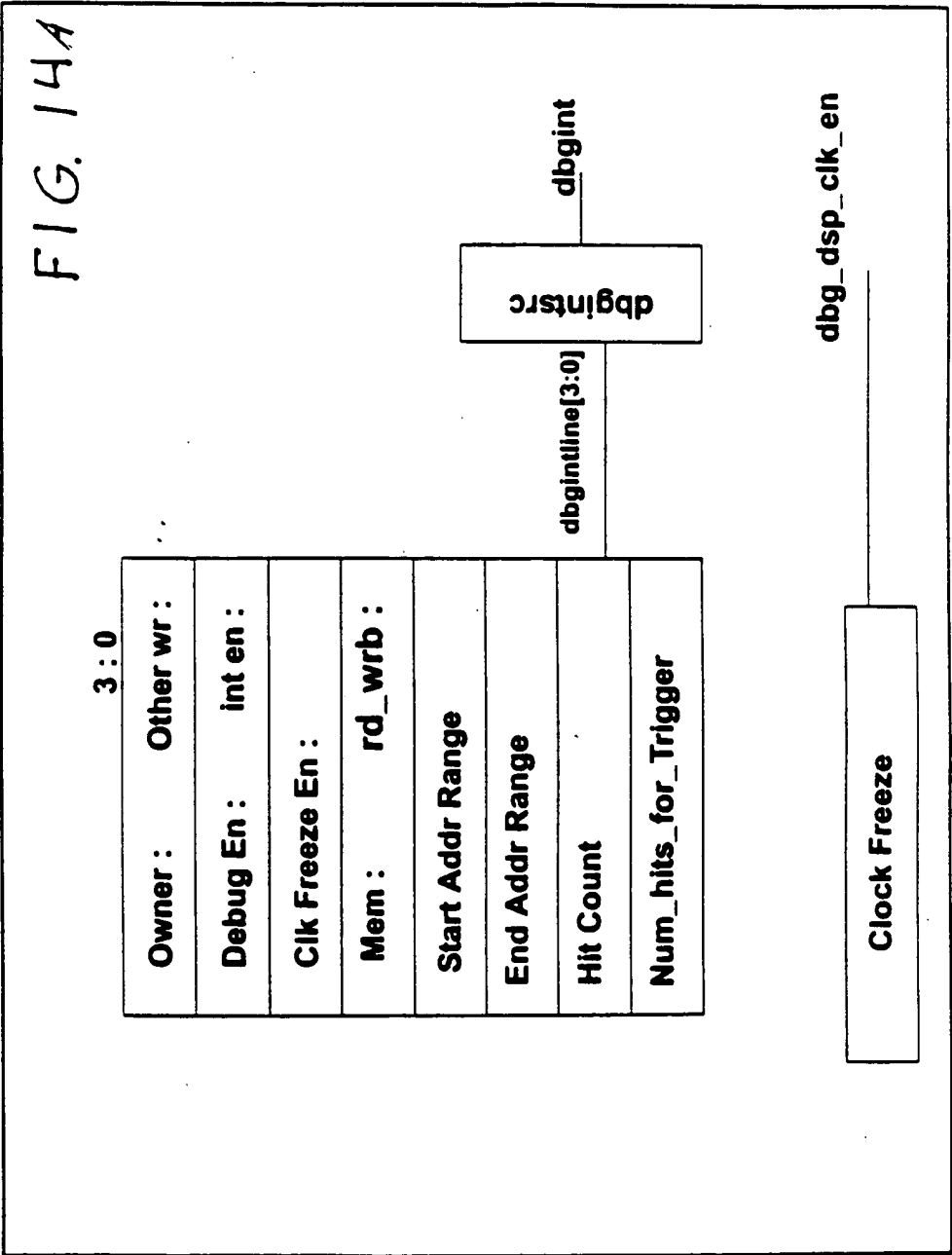


Fig. 14B

Sheet of 3

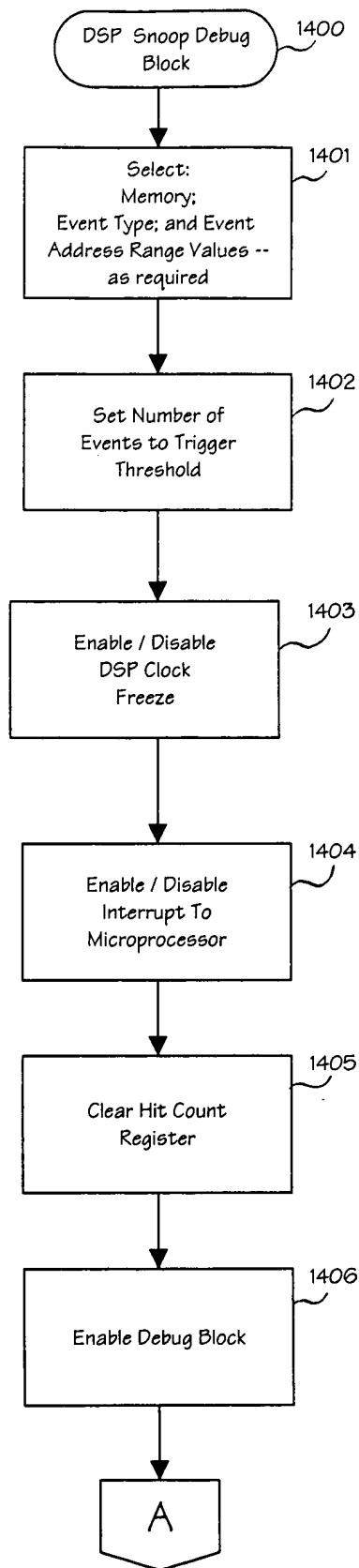


FIG. 14B

Fig 14B  
Sheet 2 of 3

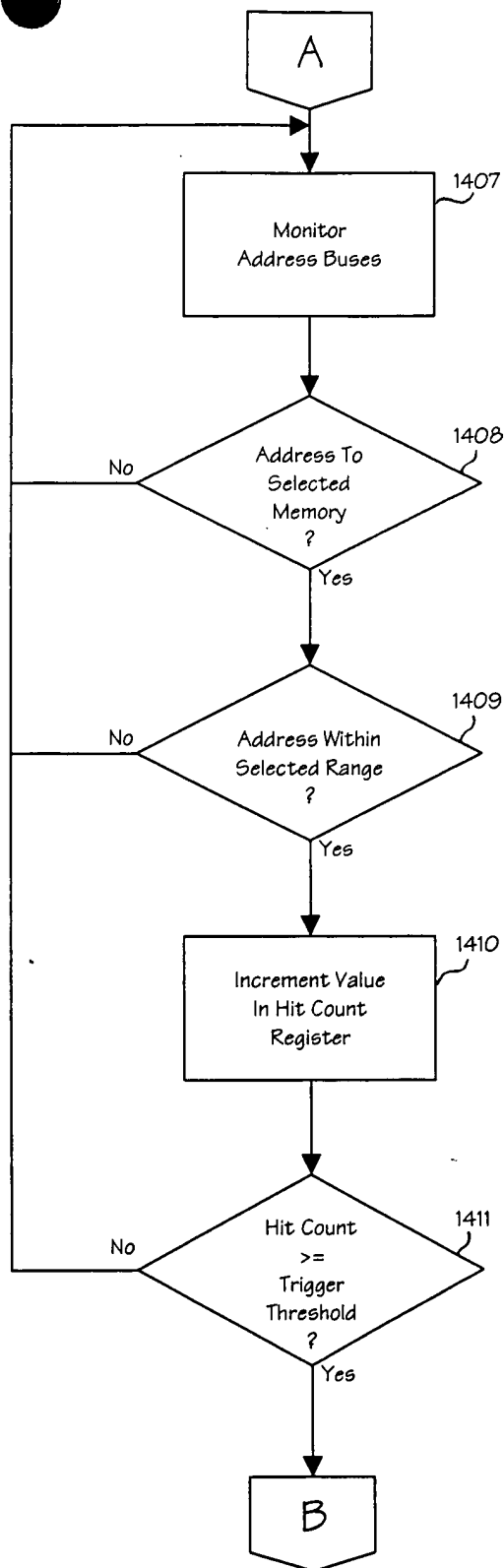
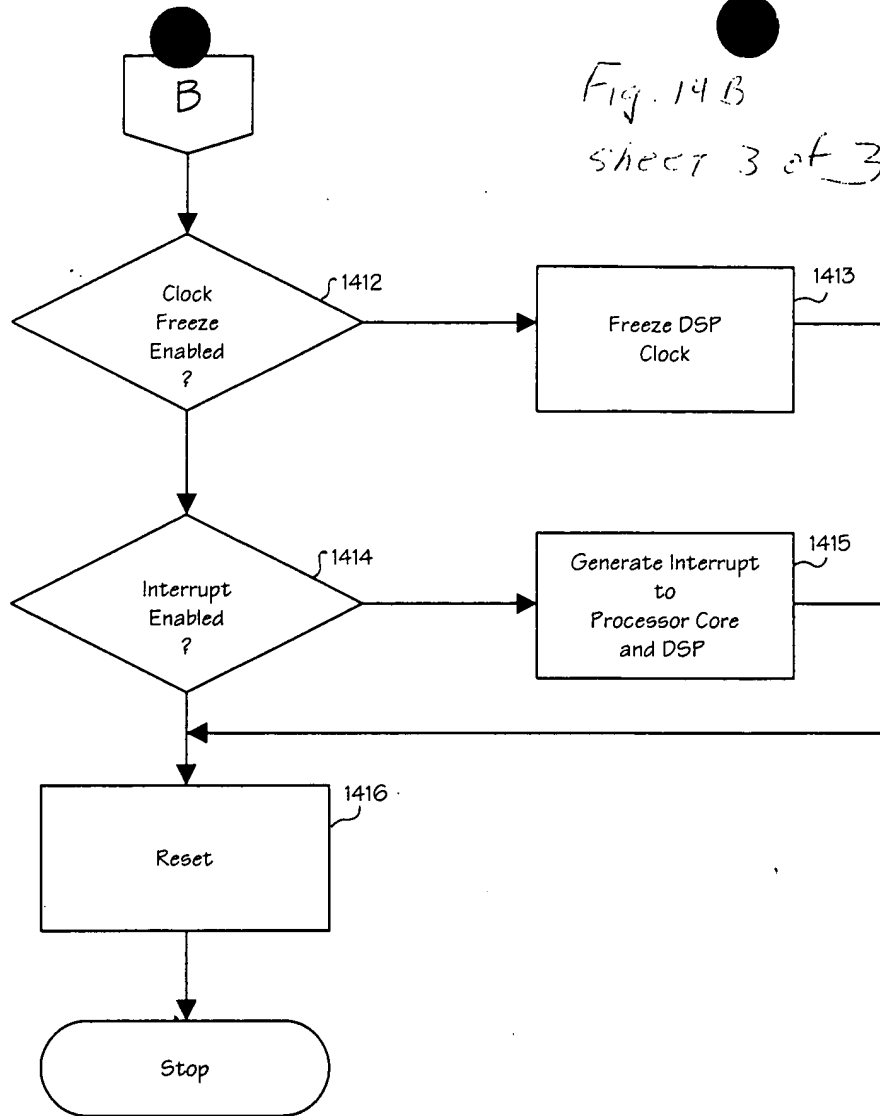


Fig. 14B  
sheet 3 of 3





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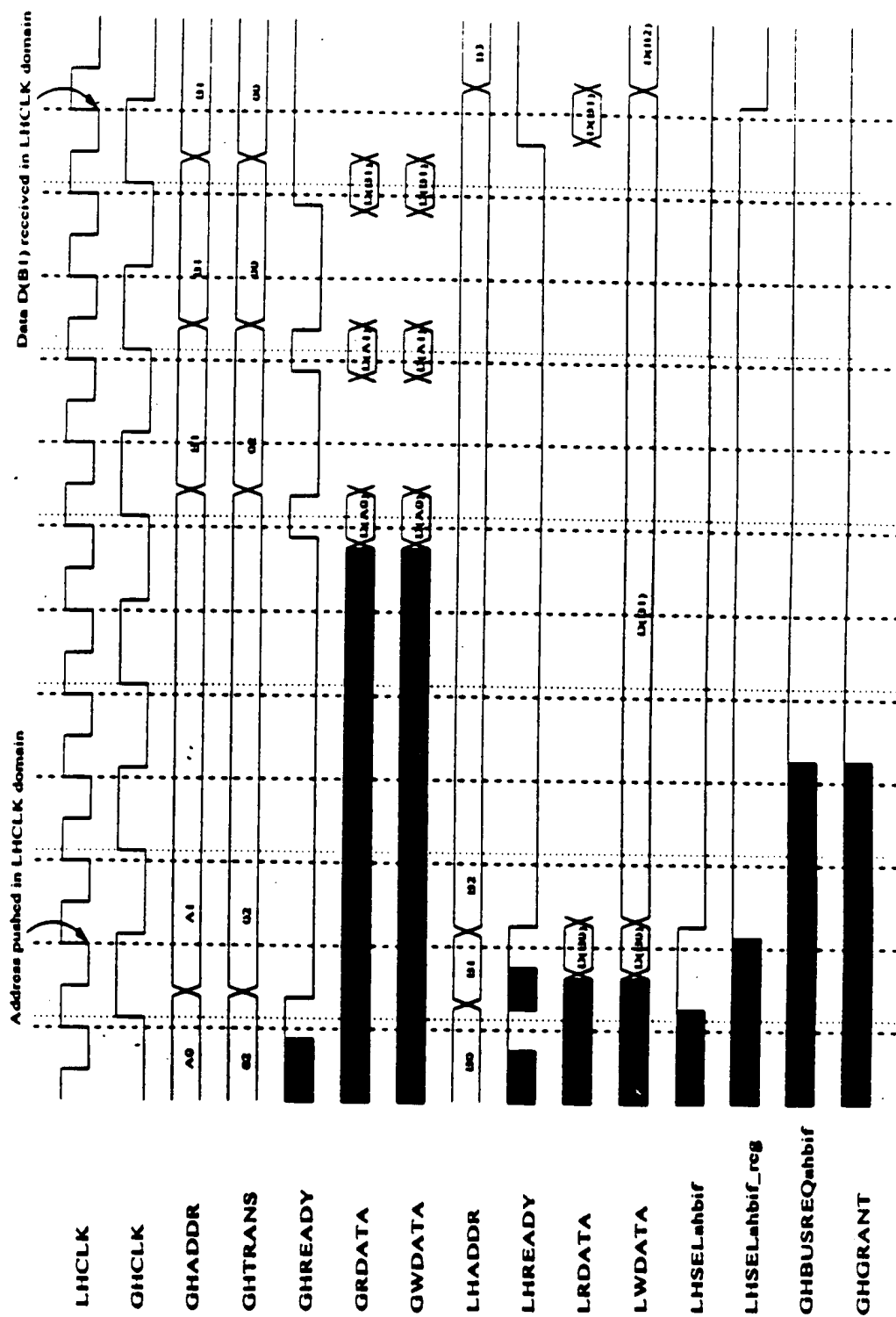
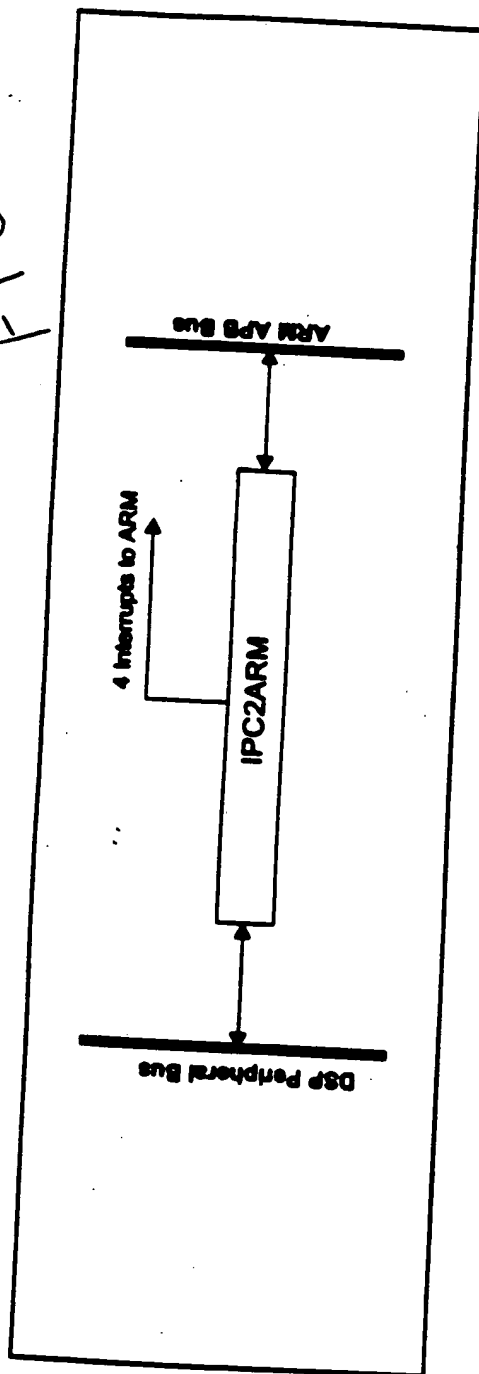
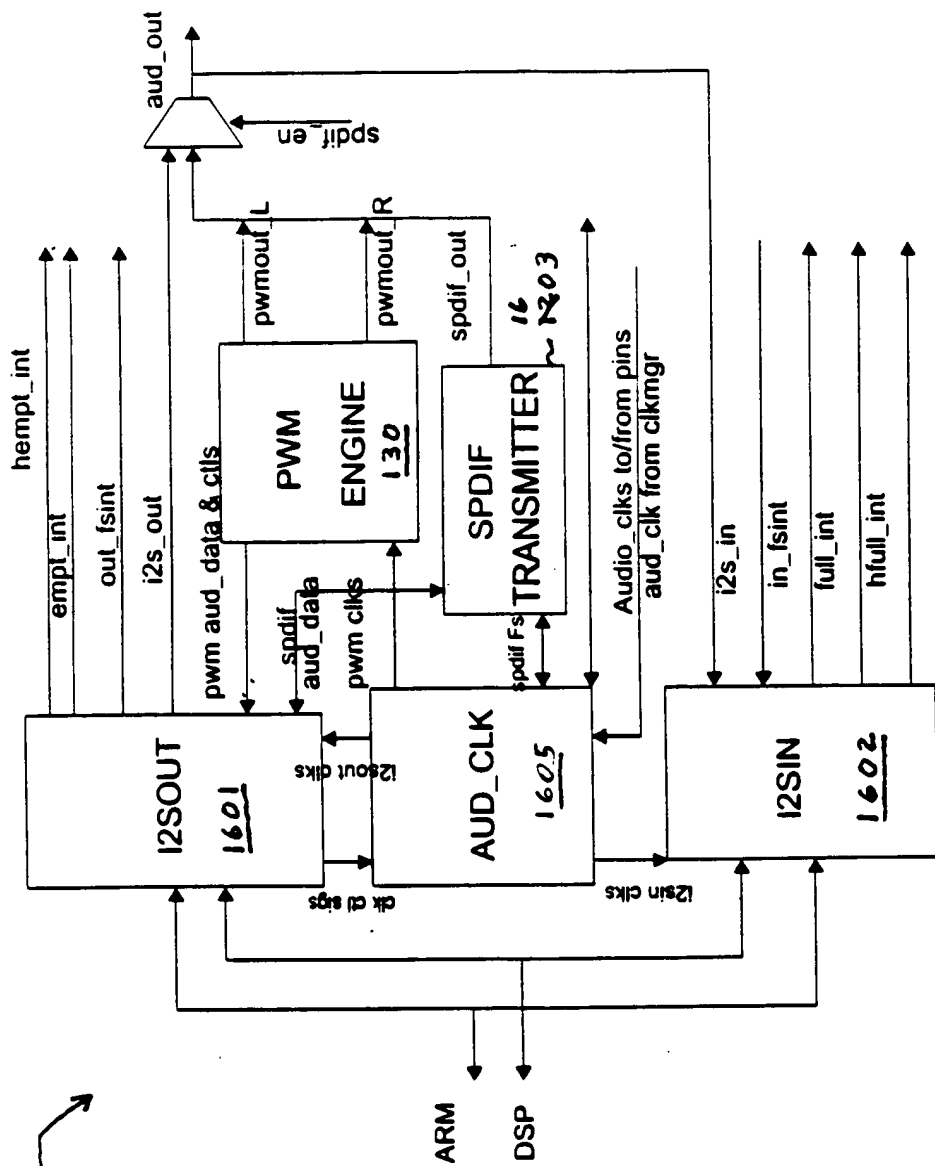


FIG 2D

FIG. 15



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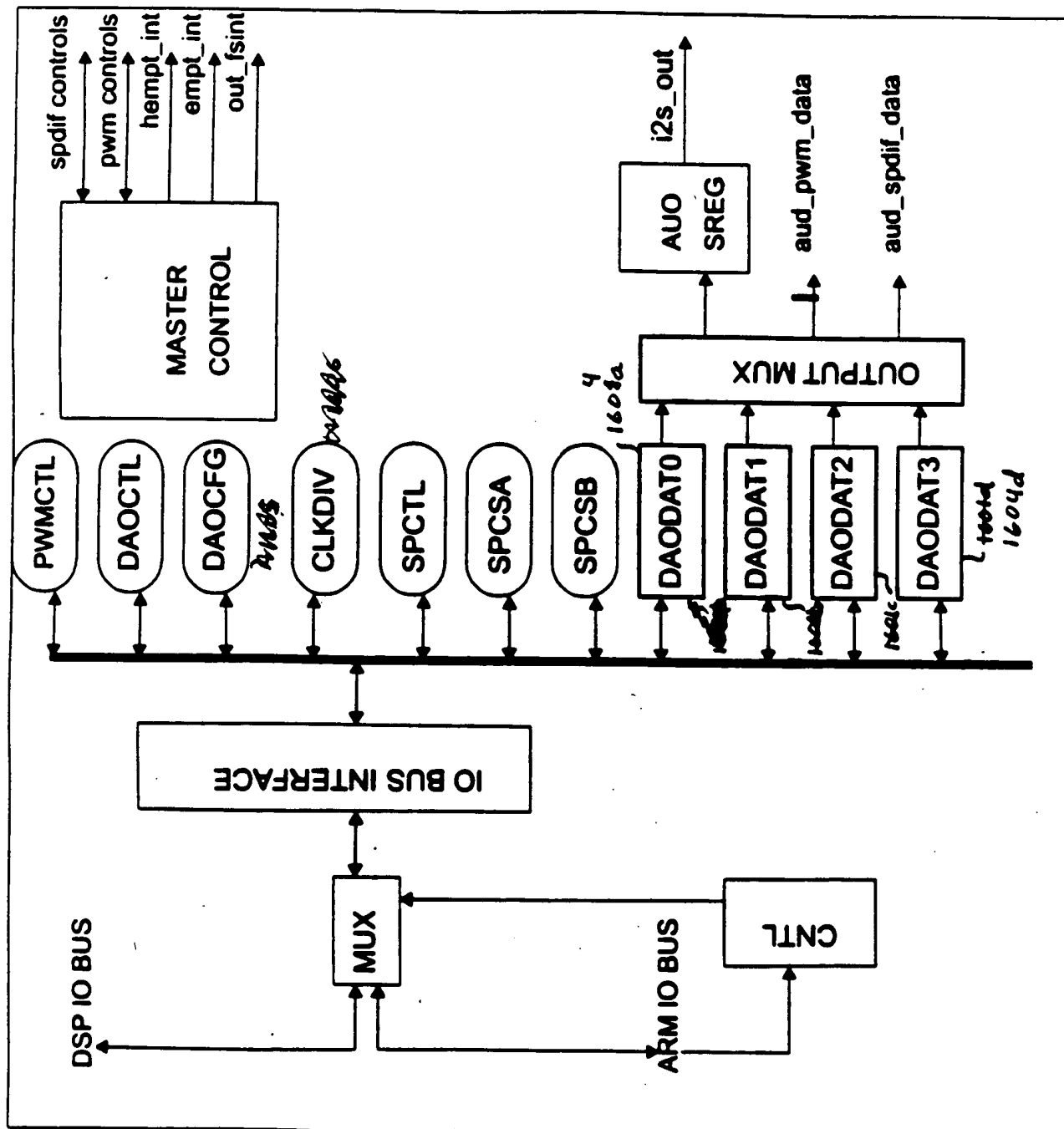
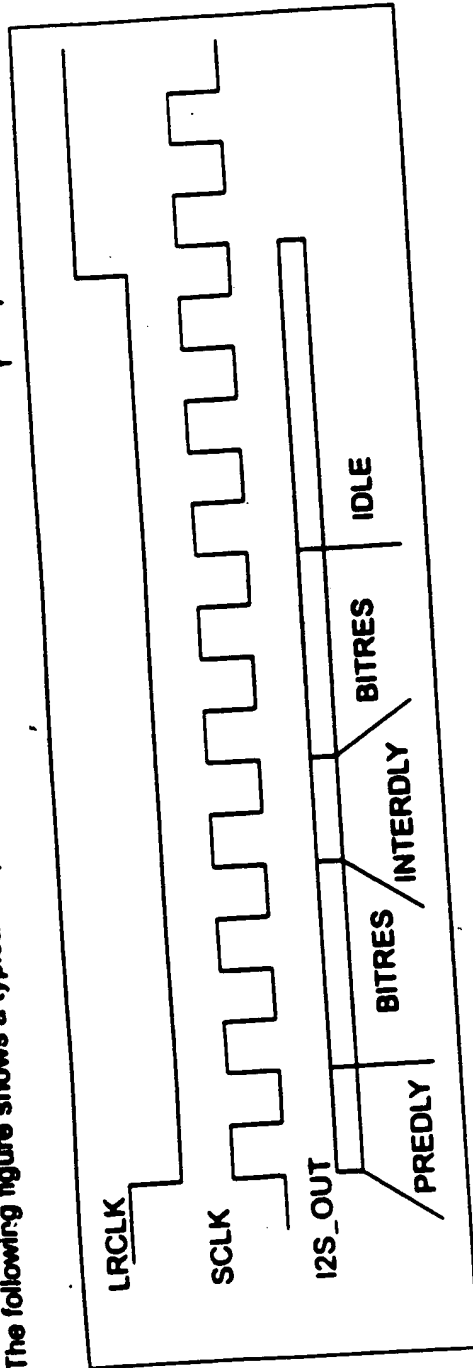


FIG. 16B

FIG. 16C

The following figure shows a typical output waveform.



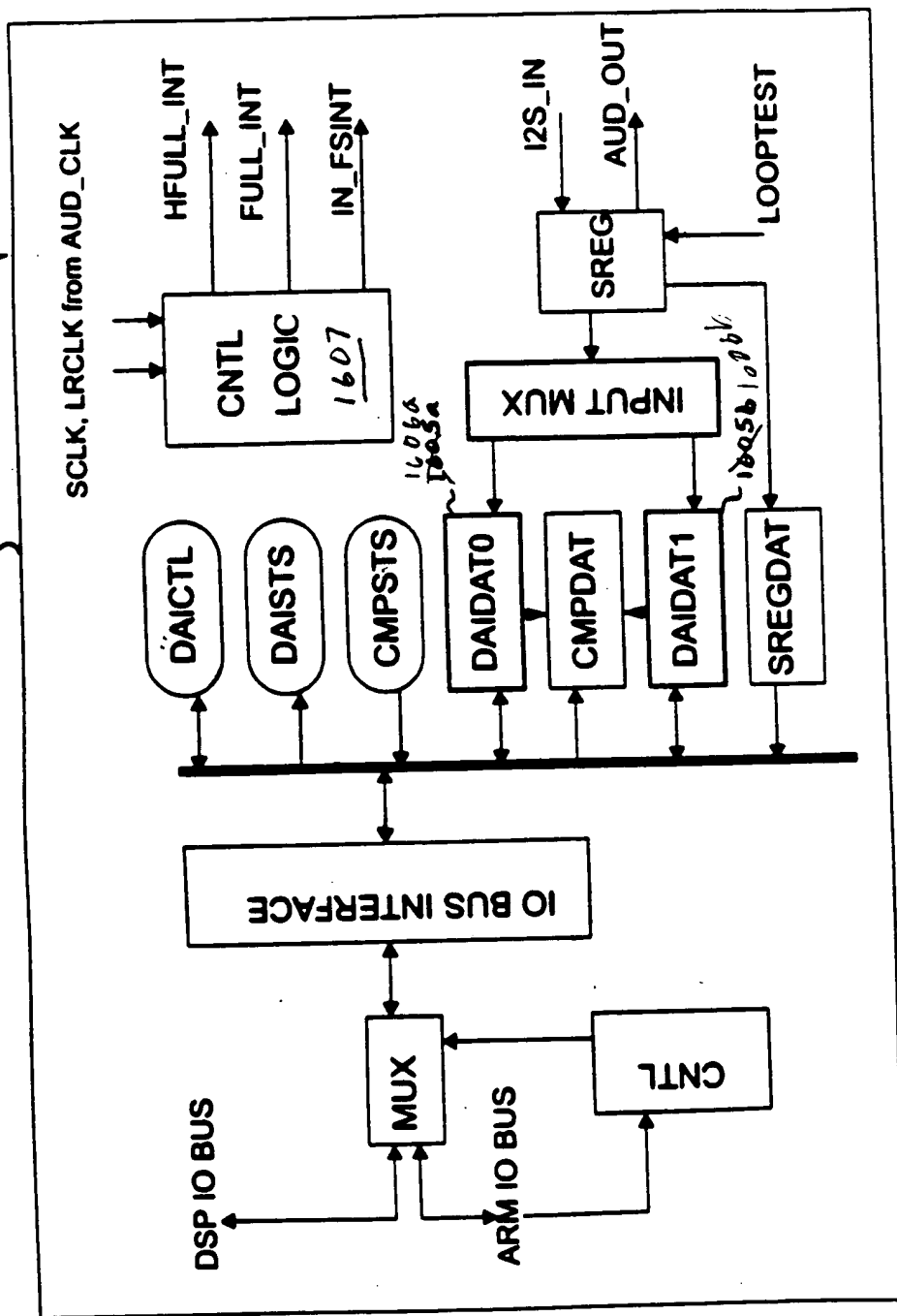


FIG. 17

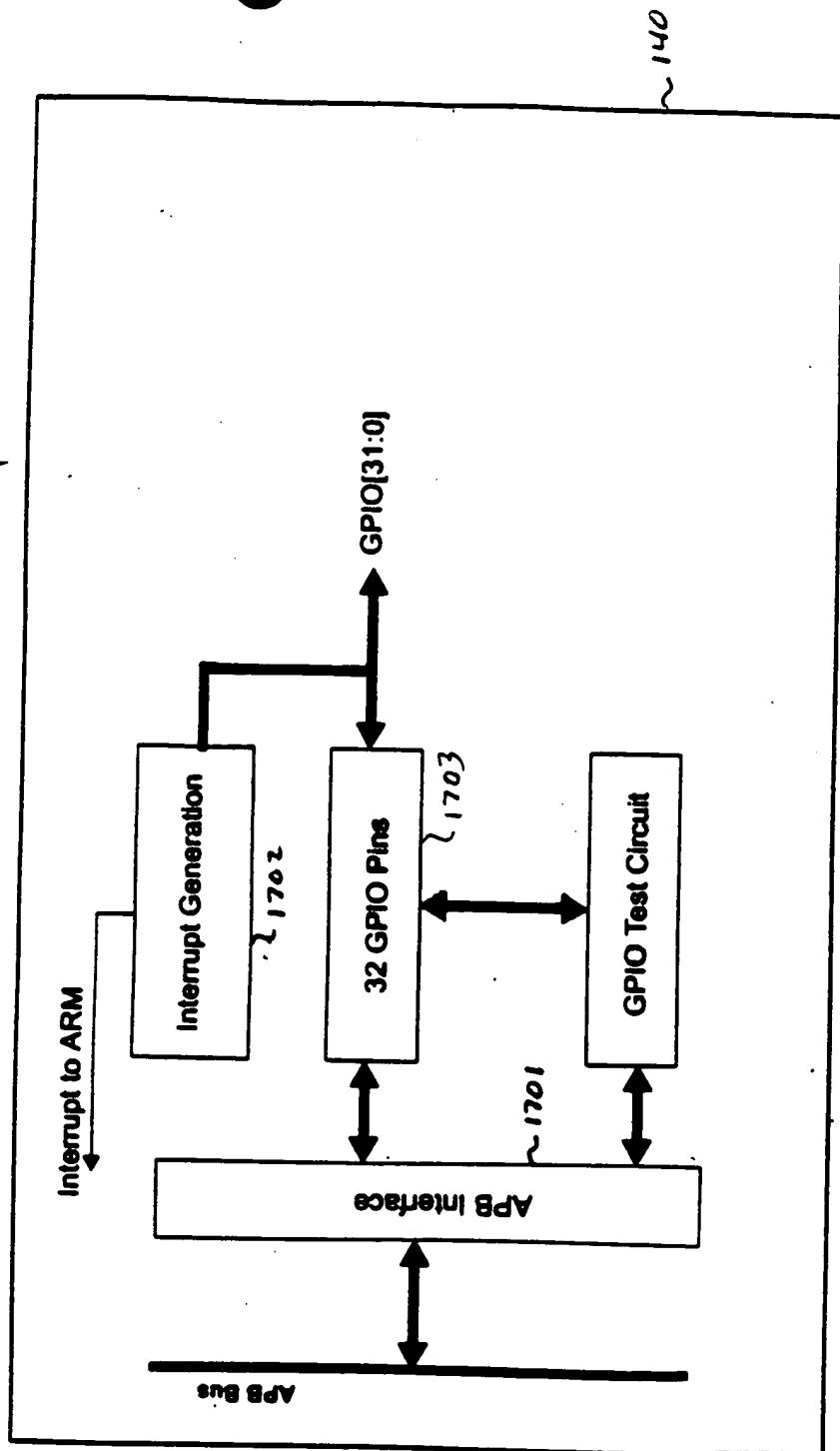
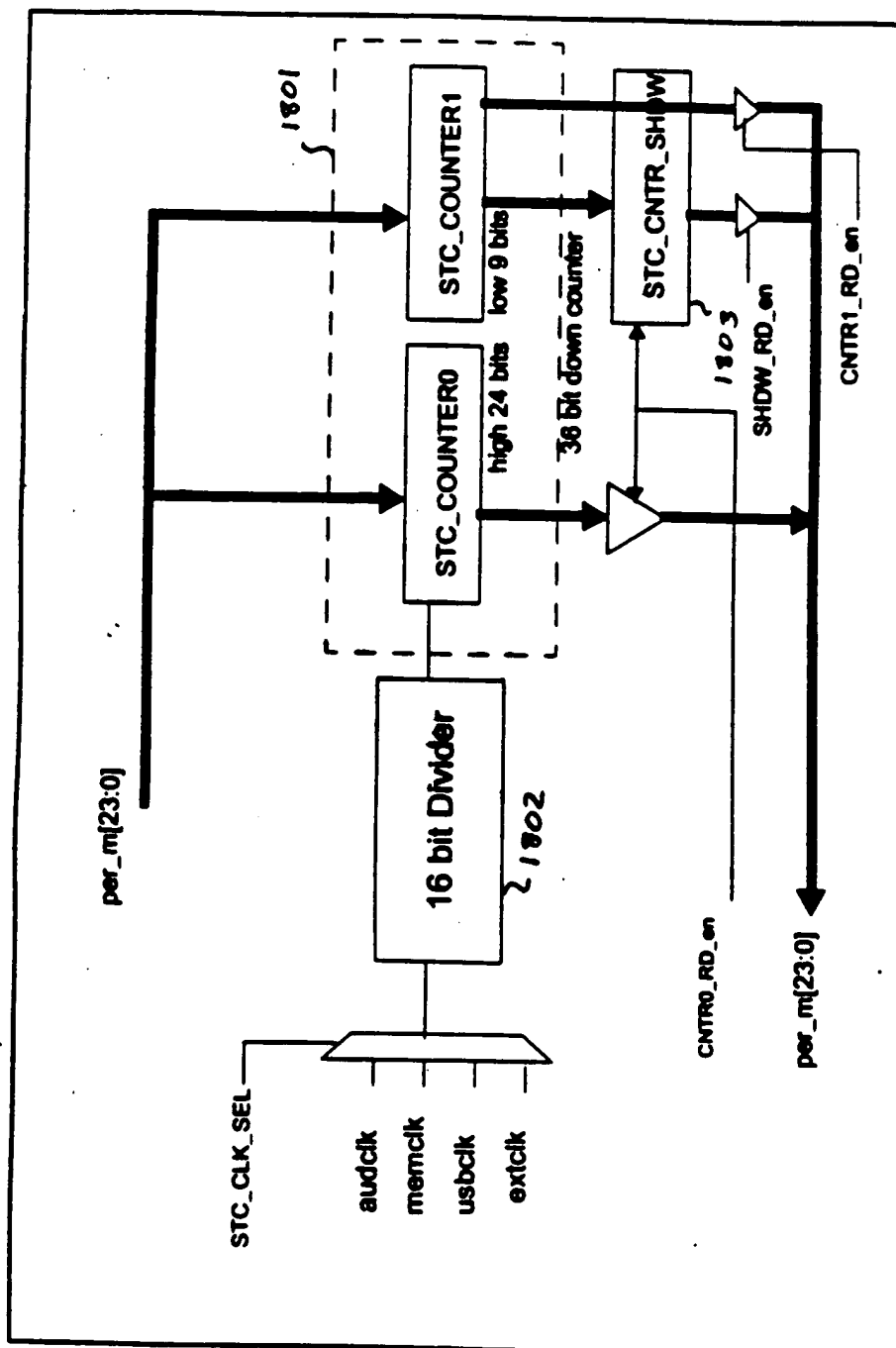


FIG. 18





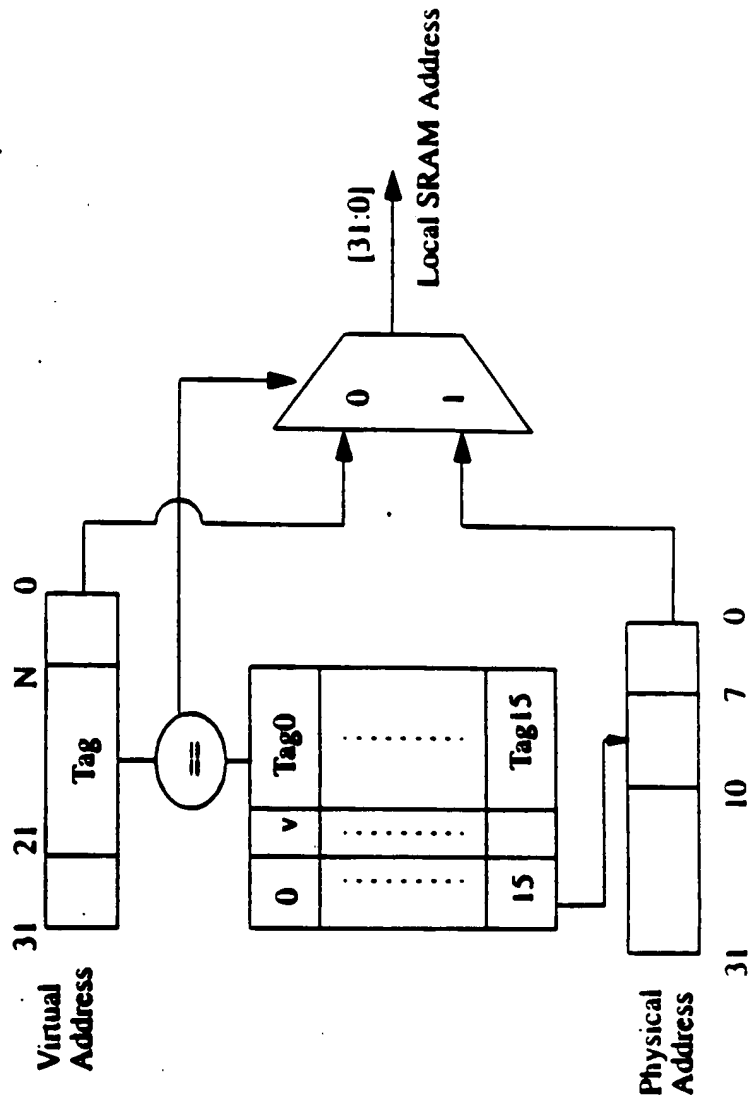
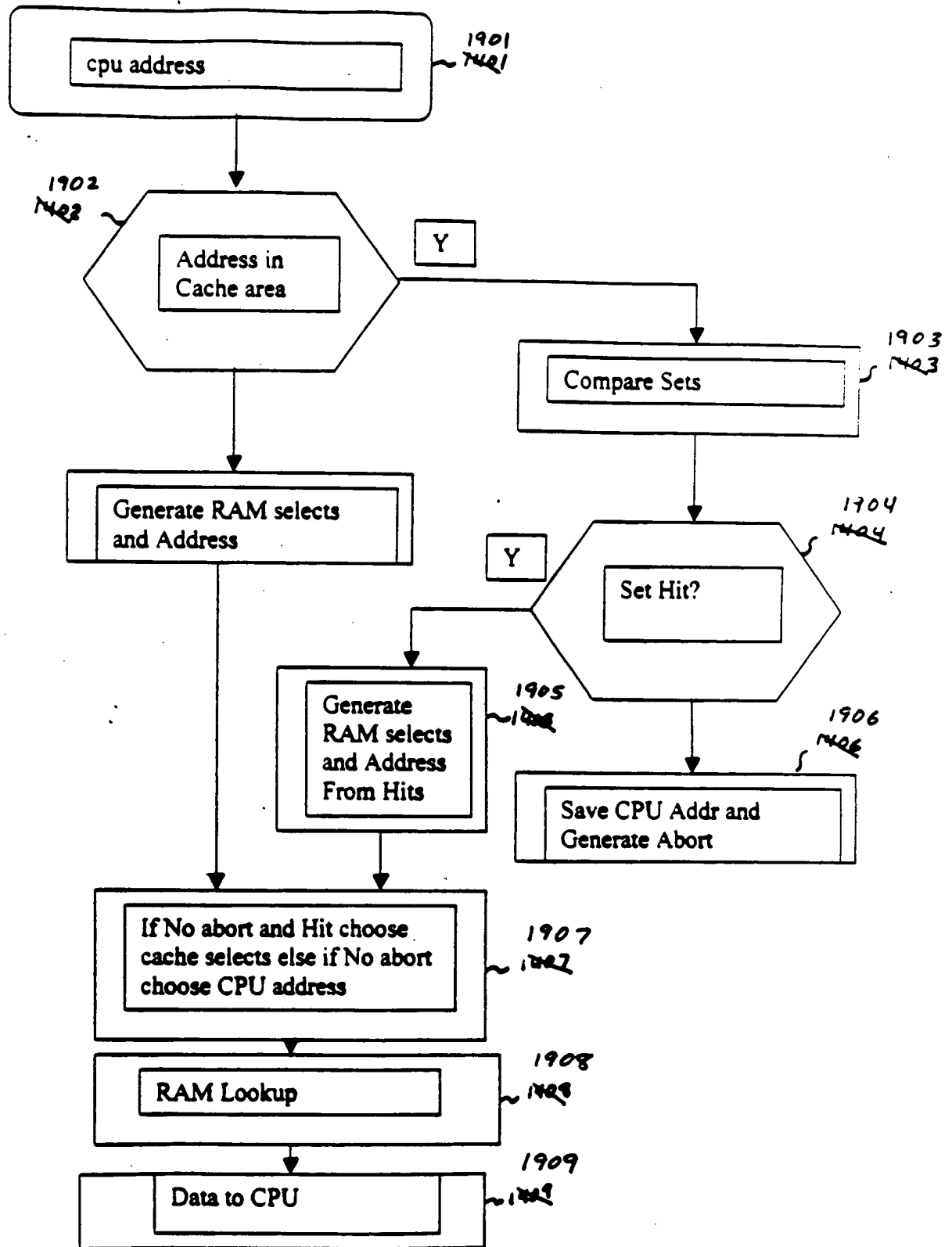


FIG 19 B



FOUO-2502280

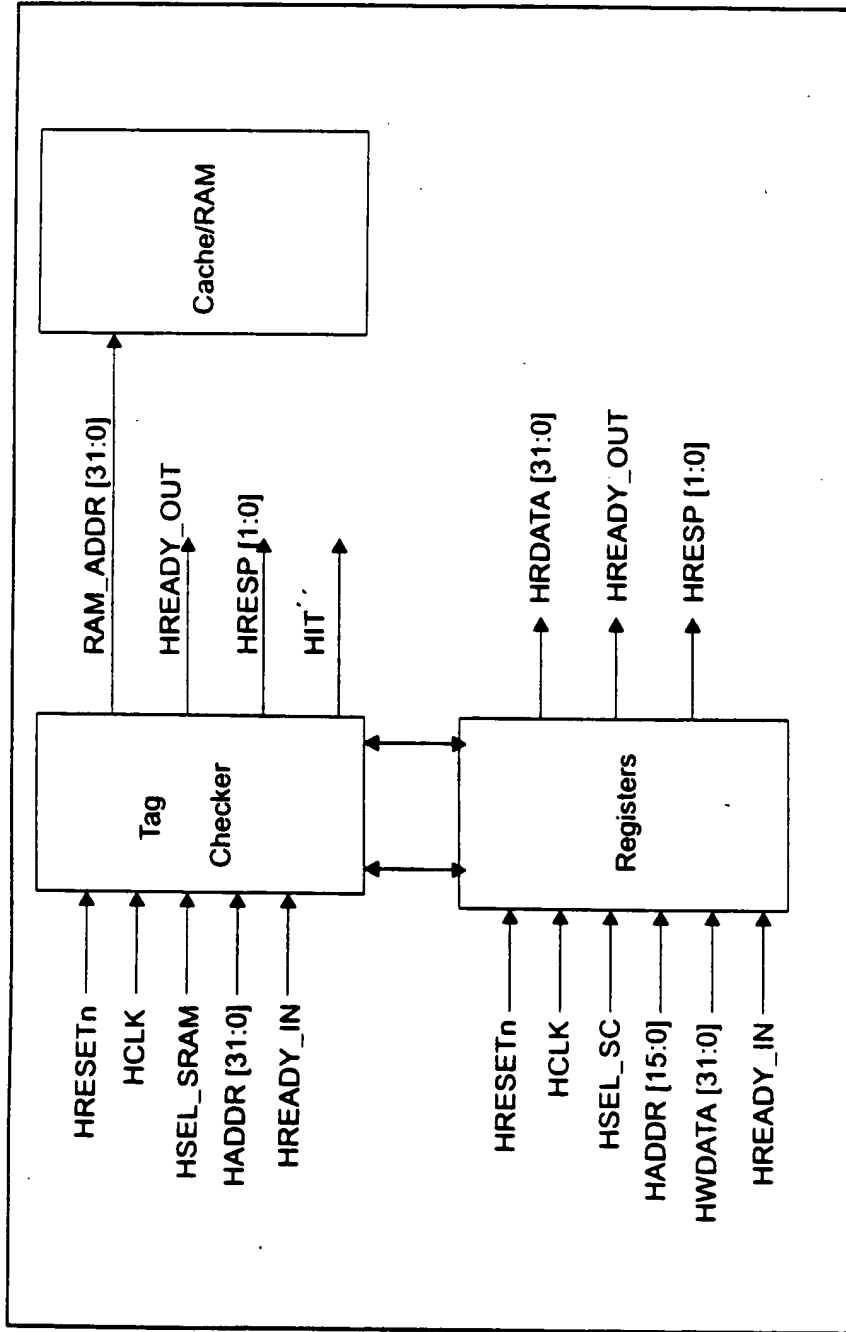


FIG. 19C

Fig. 20A

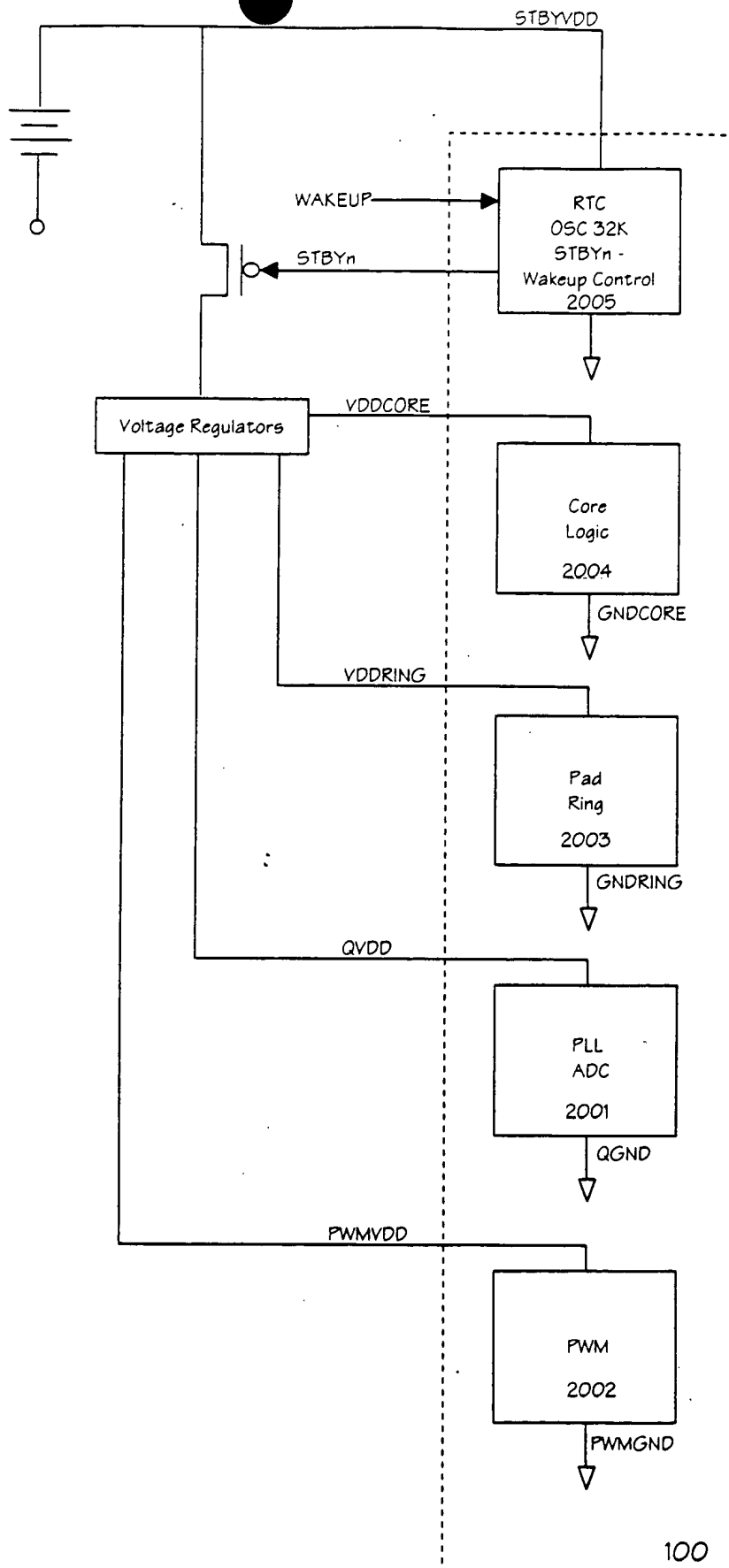


FIG. 20B

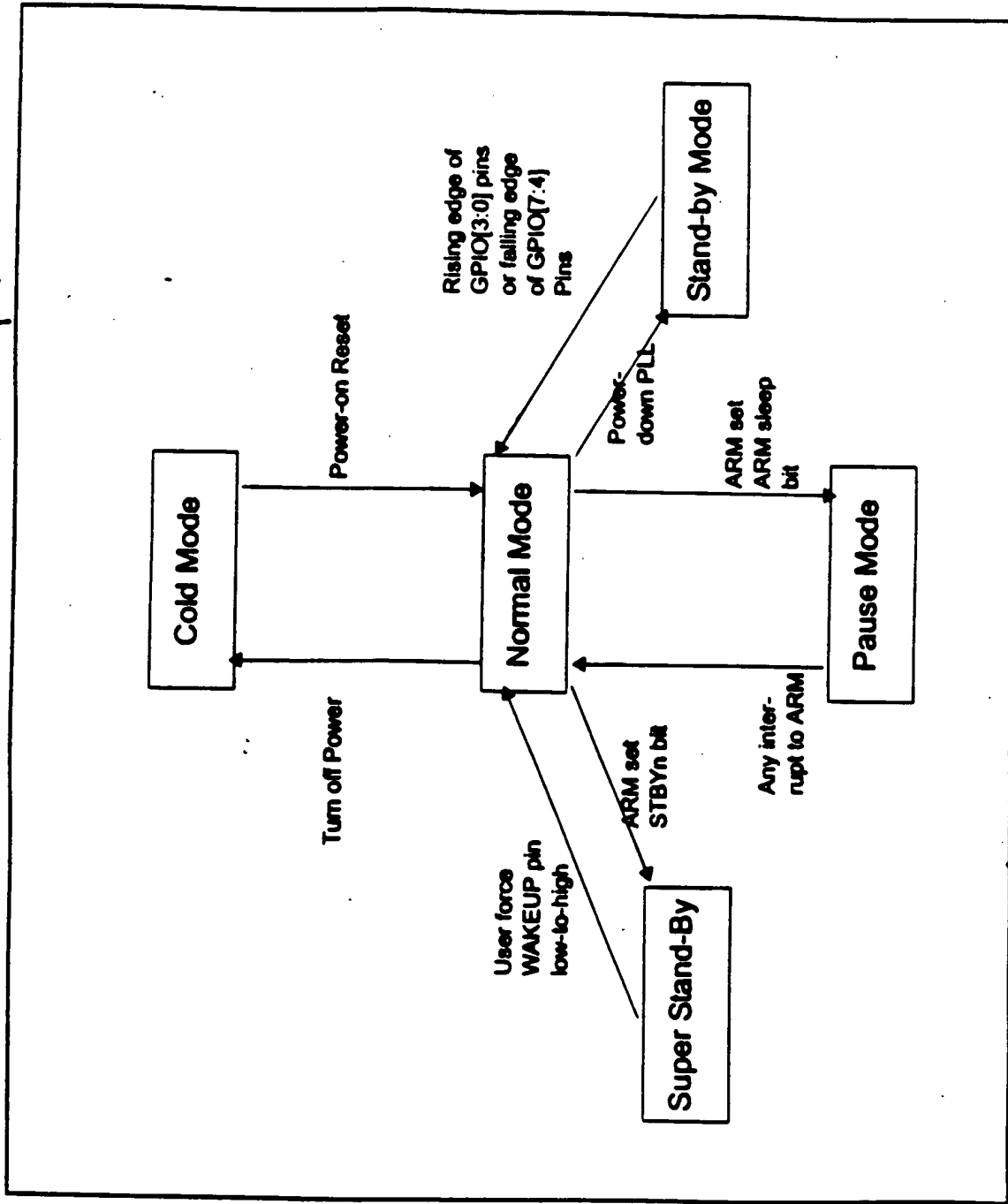
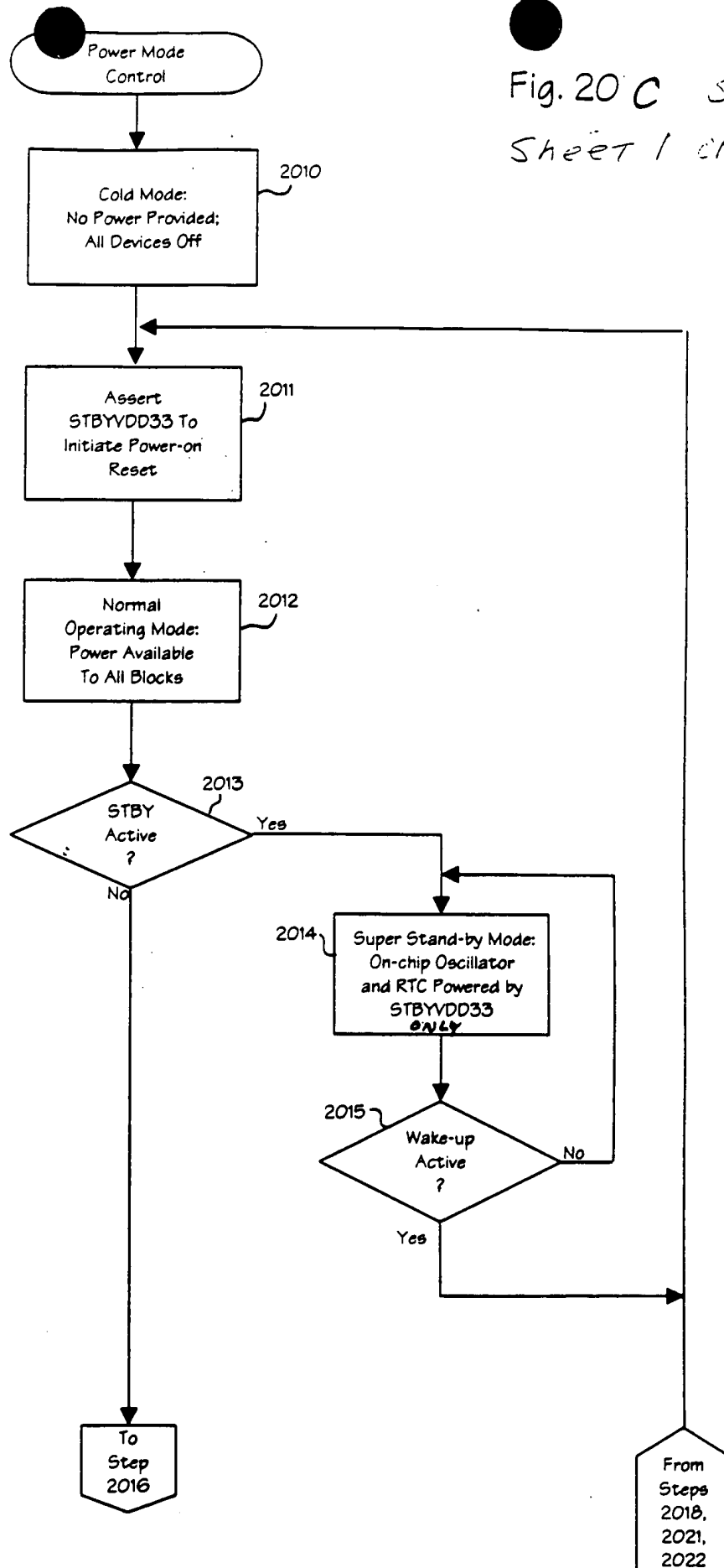


Fig. 20 C 5  
Sheet 1 of 3



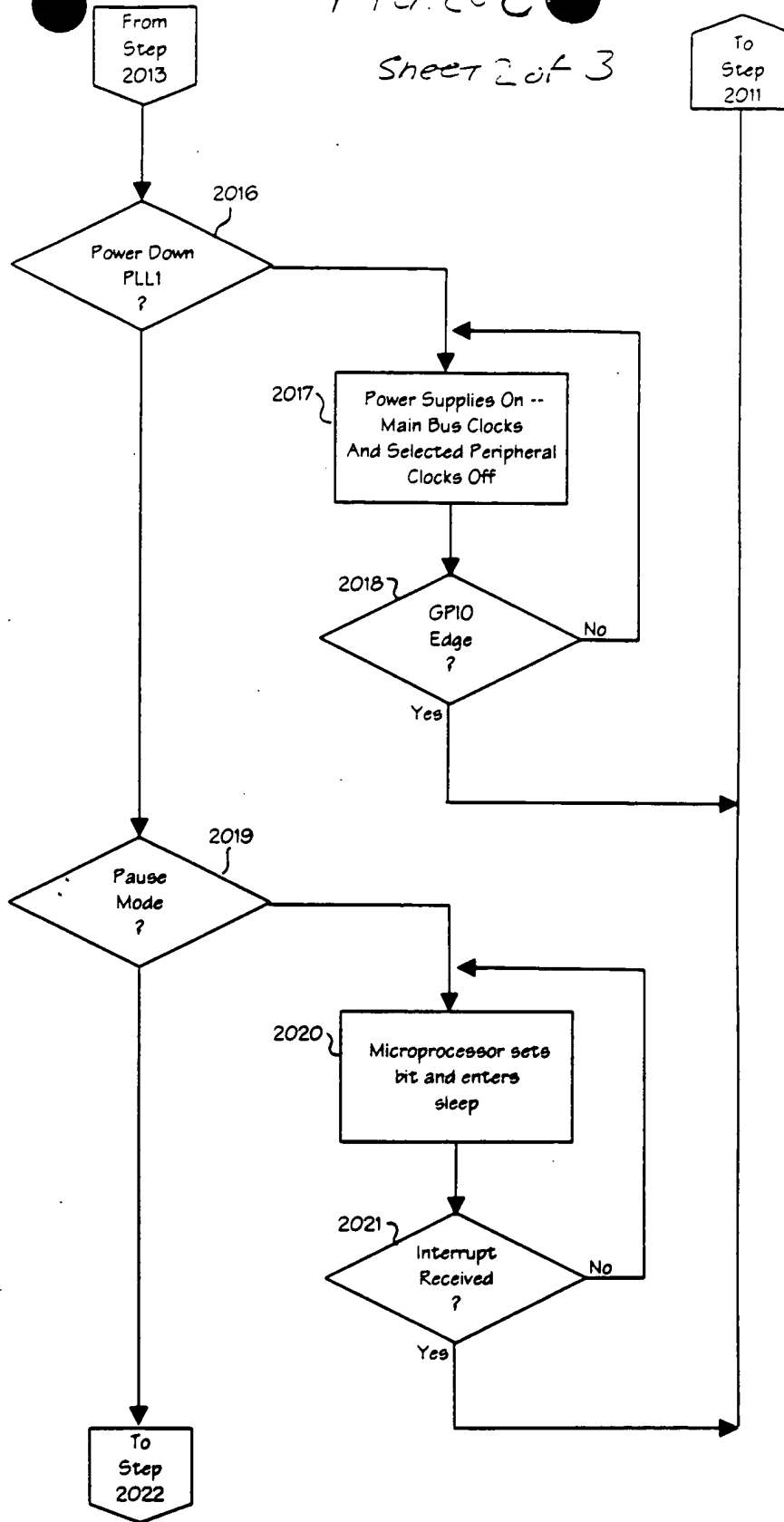


FIG. 20C = 0902280

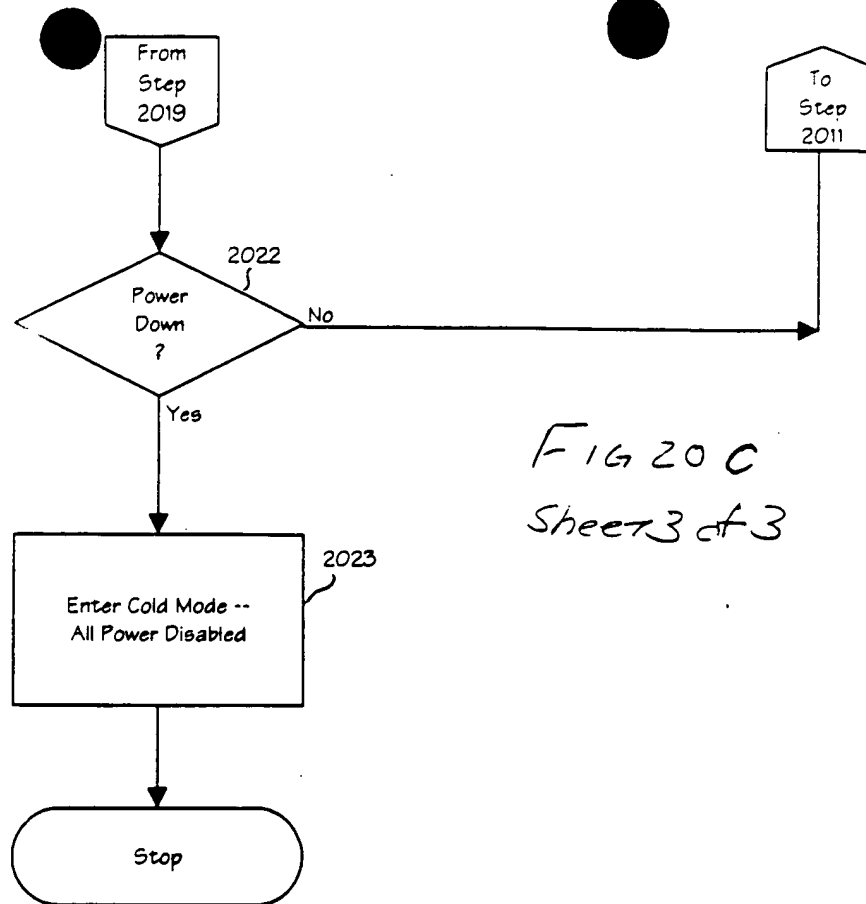
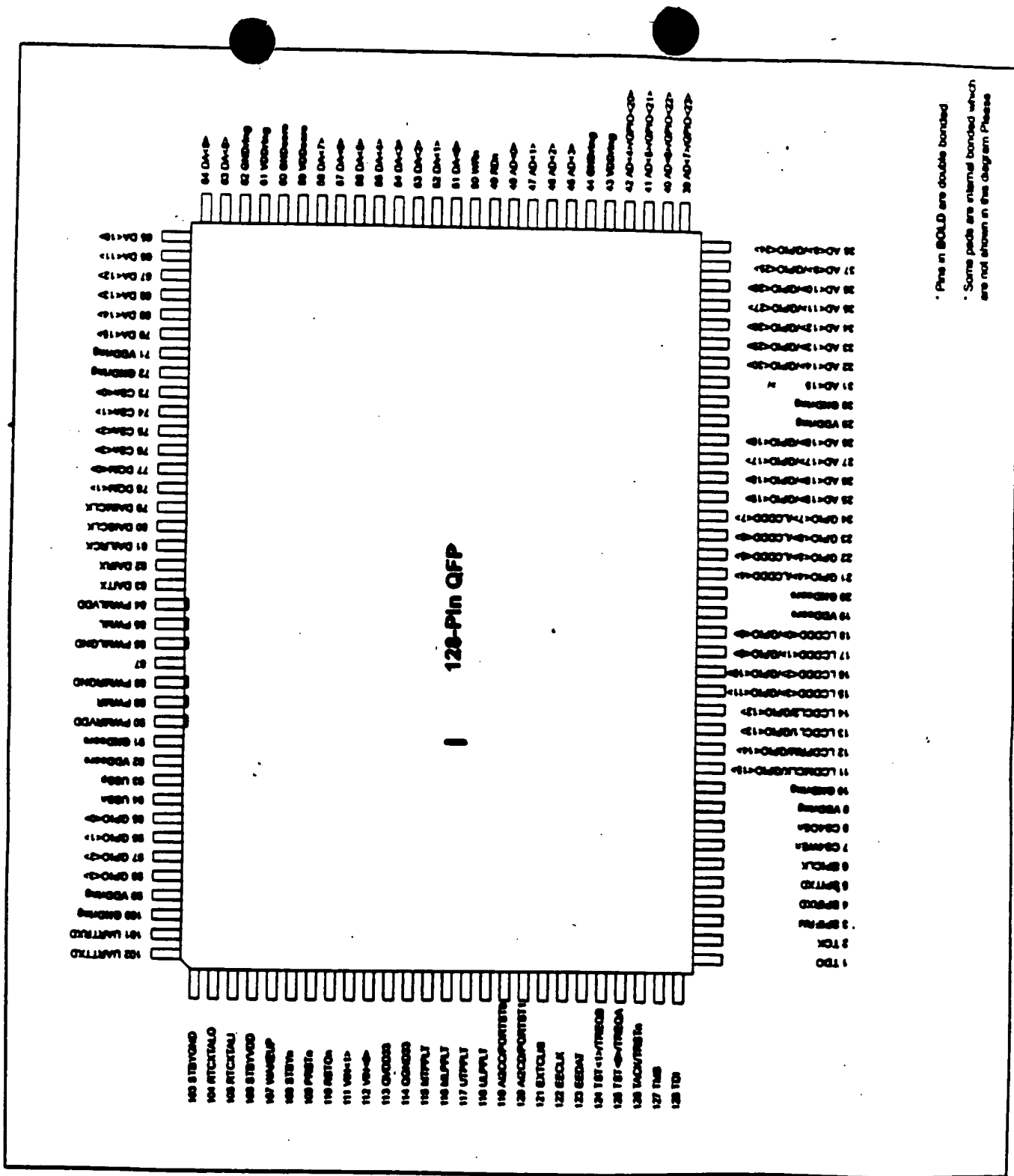


FIG 20C  
Sheet 3 of 3

FIG 20C Sheet 3 of 3



FIG 21



\* Pins in **BOLD** are double bonded

\* Some pads are internal bonded which are not shown in this diagram. Please